

PCB STACK UP

8L

UT6 BLOCK DIAGRAM

01

LAYER 1 : TOP
LAYER 2 : SGND
LAYER 3 : IN1
LAYER 4 : SVCC
LAYER 5 : IN2
LAYER 6 : IN3
LAYER 7 : SGND1
LAYER 8 : BOT

Cable Docking

VGA
RJ-45
CIR/Pwr btn
SPDIF Out
Stereo MIC
Headphone Jack
USB Port
VOL Cntr

PAGE 38

SYSTEM CHARGER ISL6251AHAZ-T
PAGE 39

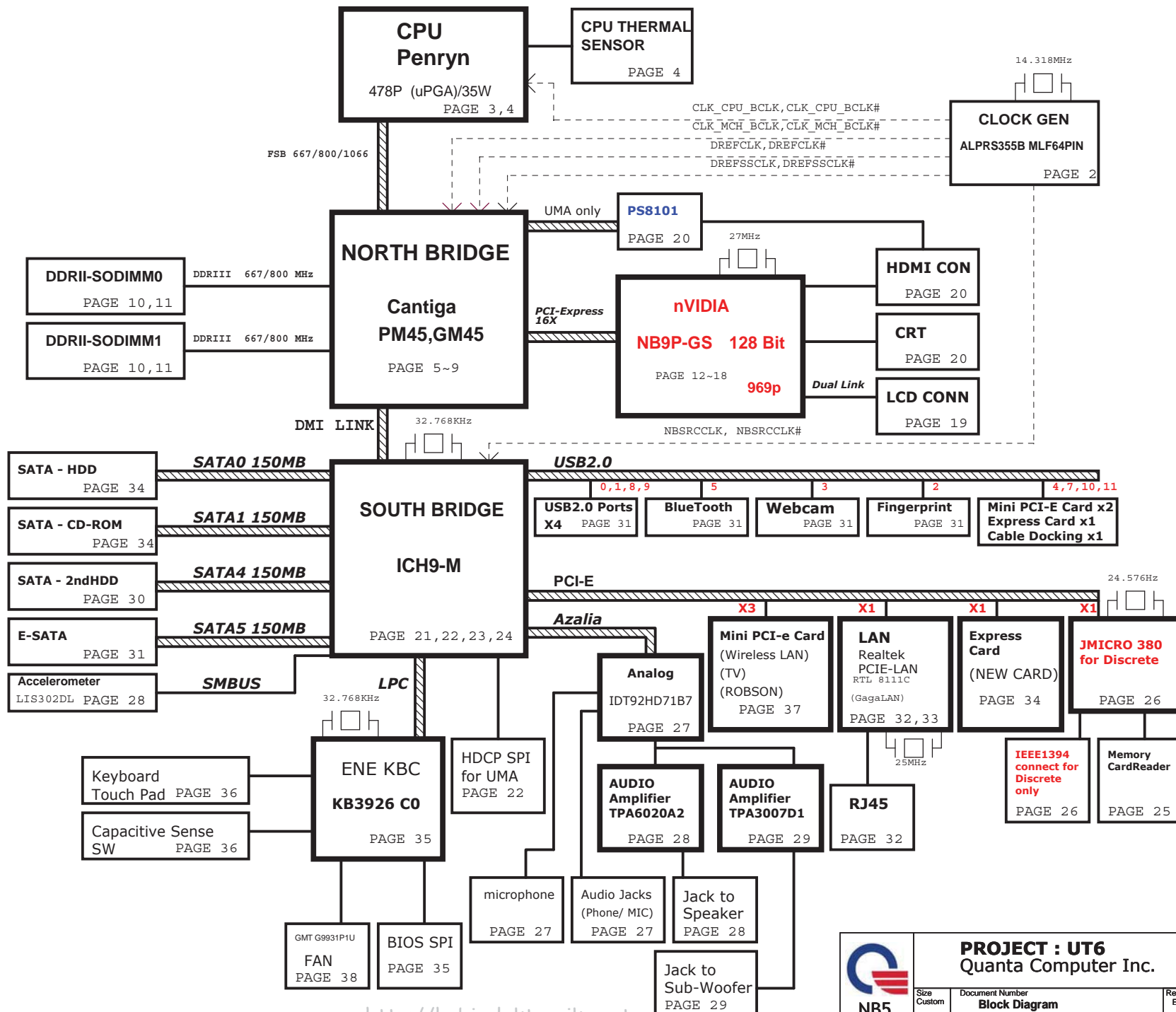
SYSTEM POWER ISL6237IRZ-T
PAGE 40

DDR II SMDDR_VTERM
1.8V/1.8VSUS(TPS51116REGR)
PAGE 44

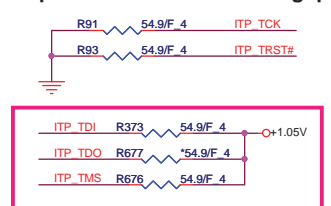
VCCP +1.5V AND GMCH
1.05V(RT8204)
PAGE 44

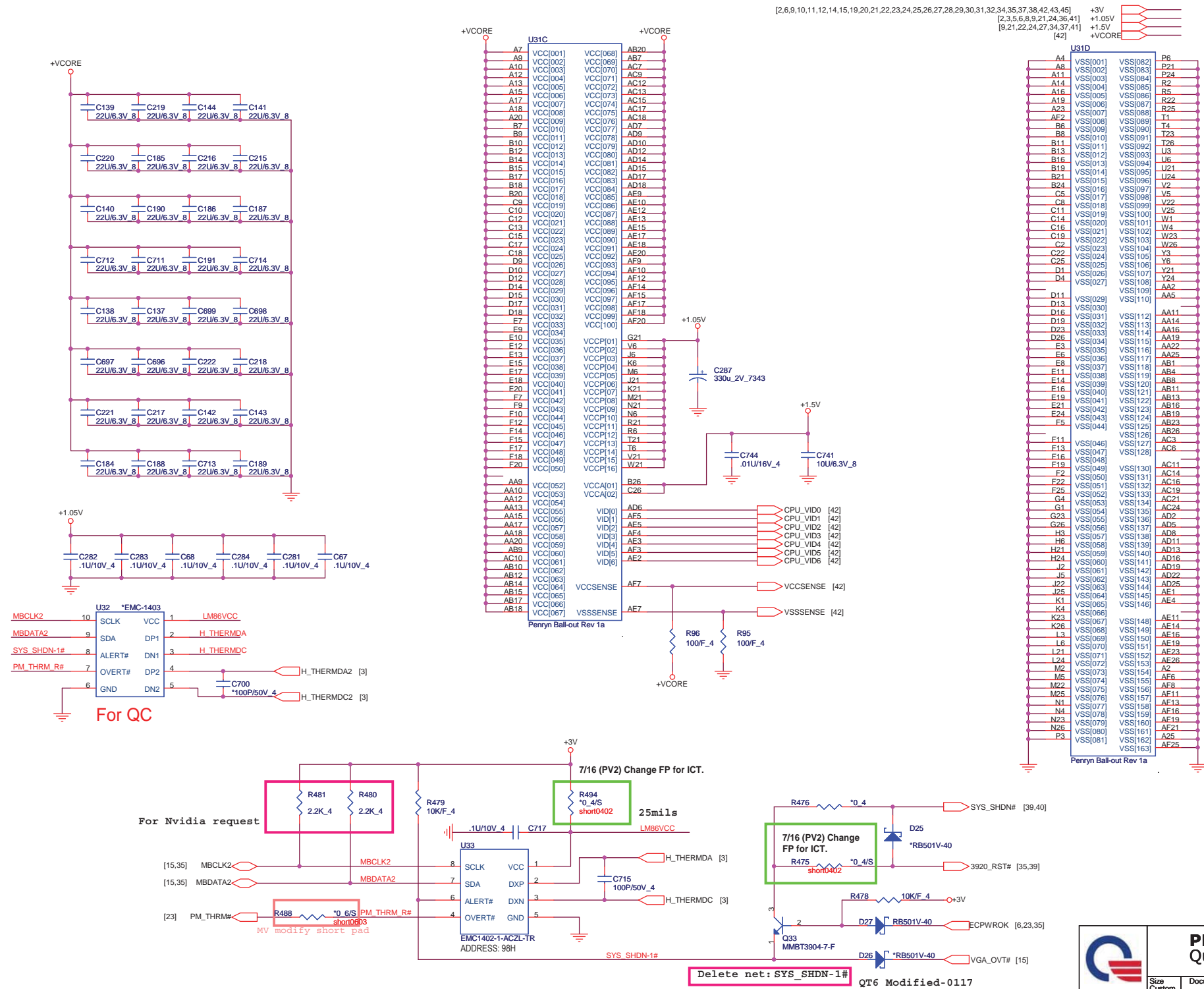
VGACORE(1.025V)Oz8118
PAGE 43

CPU CORE ISL6266A
PAGE 42











MCH_CFG_5 DMIx2 selection
Low = DMI X2
High = DMI X4 (Default)
MCH_CFG_16 FSB Dynamic ODT
Low = Dynamic ODT disabled
High = Dynamic ODT enabled (default)
MCH_CFG_9 PCI Express Graphic Lane
Low: Reverse Lane
High: Normal operation(Default)
MCH_CFG_19 DMI Lane Reversal
Low = Normal operation (Default)
High = Reverse Lanes

MCH_CFG_6 ITPM Host Interface
Low = The ITPM Host Interface is enabled2
High = The ITPM Host Interface is disabled (default)

MCH_CFG_7 Intel(R) Management Engine Crypto
Low: Intel(R) Management Engine Crypto
High: Intel(R) Management Engine Crypto

MCH_CFG_10 PCIe Lookback Enable
Low = Enabled3
High: Disabled (Default)

MCH_CFG_12/13 XOR/ALL/CLOCK Un-gating

MCH_CFG_13 **MCH_CFG_12** Configuration

0	0	Reserved
0	0	XOR Mode enabled
0	1	All-Z Mode enabled
1	1	Normal operation (Default)

Digital Display Port (SDVO/DP/HDMI) Concurrent with PCIe
Low = Only digital display port (SDVO/DP/HDMI) or PCIe is operational (default)
High = Digital display port (SDVO/DP/HDMI) and PCIe are operating simultaneously via the PEG port

MCH_CFG_2:0

000 = FSB1066
010 = FSB800
011 = FSB667
Others = Reserved

MCH_CFG_3 **MCH_CFG_4** **MCH_CFG_5** **MCH_CFG_6** **MCH_CFG_7** **MCH_CFG_8** **MCH_CFG_9** **MCH_CFG_10** **MCH_CFG_11** **MCH_CFG_12** **MCH_CFG_13** **MCH_CFG_14** **MCH_CFG_15** **MCH_CFG_16** **MCH_CFG_17** **MCH_CFG_18** **MCH_CFG_19** **MCH_CFG_20**

MCH_CFG_21 **MCH_CFG_22** **MCH_CFG_23** **MCH_CFG_24** **MCH_CFG_25** **MCH_CFG_26** **MCH_CFG_27** **MCH_CFG_28** **MCH_CFG_29** **MCH_CFG_30** **MCH_CFG_31**

MCH_CFG_32 **MCH_CFG_33** **MCH_CFG_34** **MCH_CFG_35** **MCH_CFG_36** **MCH_CFG_37** **MCH_CFG_38** **MCH_CFG_39** **MCH_CFG_40** **MCH_CFG_41** **MCH_CFG_42**

MCH_CFG_43 **MCH_CFG_44** **MCH_CFG_45** **MCH_CFG_46** **MCH_CFG_47** **MCH_CFG_48** **MCH_CFG_49** **MCH_CFG_50** **MCH_CFG_51** **MCH_CFG_52** **MCH_CFG_53**

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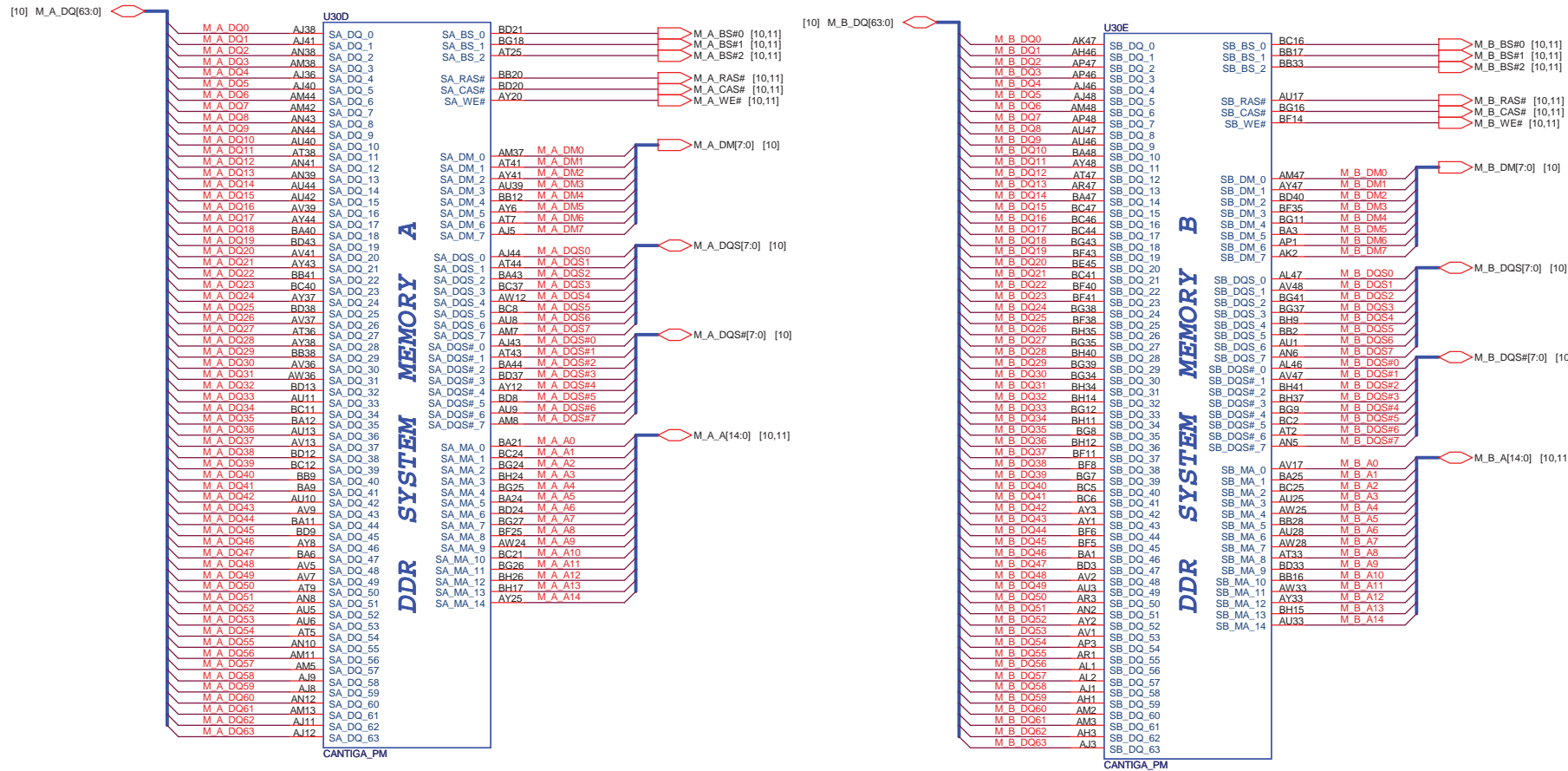
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MCH_CFG_736 **MCH_CFG_737** **MCH_CFG_738** **MCH_CFG_739** **MCH_CFG_740** **MCH_CFG_741** **MCH_CFG_742** **MCH_CFG_743** **MCH_CFG_744** **MCH_CFG_745** **MCH_CFG_746**

MCH_CFG_747 **MCH_CFG_748** **MCH_CFG_749** **MCH_CFG_750** **MCH_CFG_751** **MCH_CFG_752** **MCH_CFG_753** **MCH_CFG_754** **MCH_CFG_755** **MCH_CFG_756** **MCH_CFG_757**

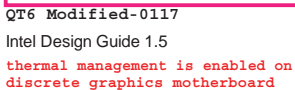
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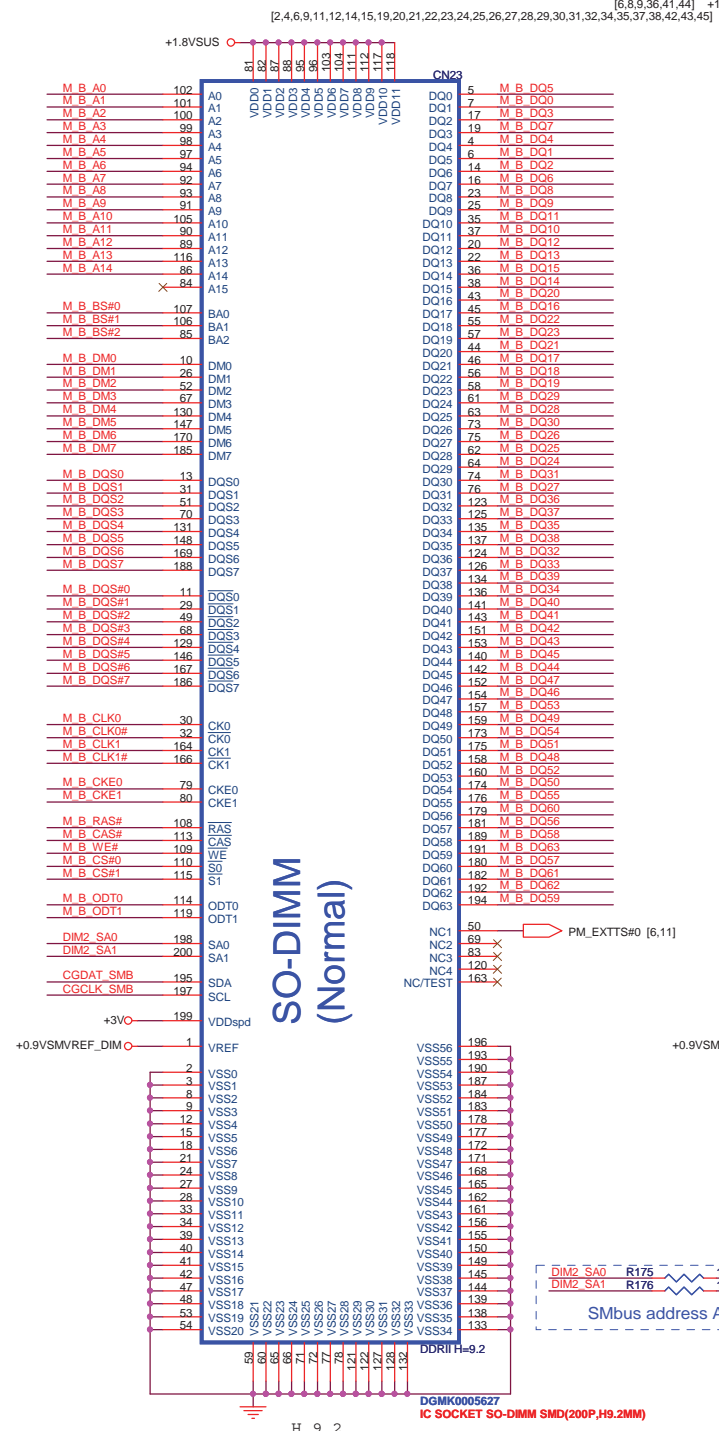


PROJECT : UT6
Quanta Computer Inc.

Size Custom Document Number Cantiga DDR2 3/5 Rev E3A
Date: Wednesday, August 06, 2008 Sheet 7 of 46

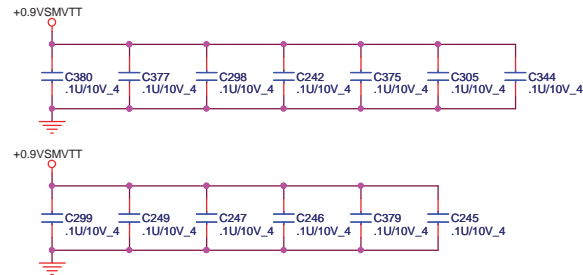






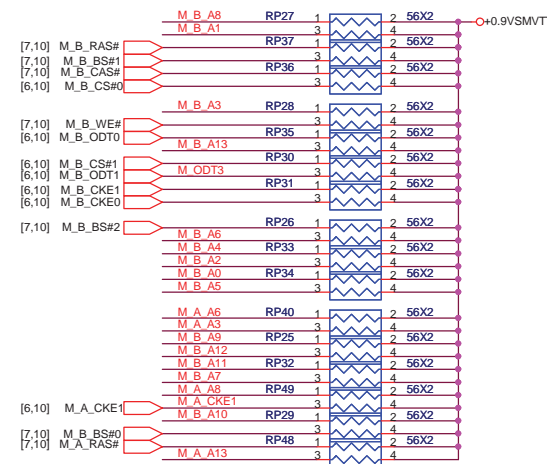
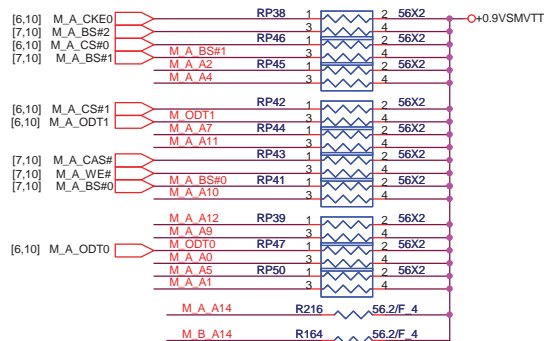
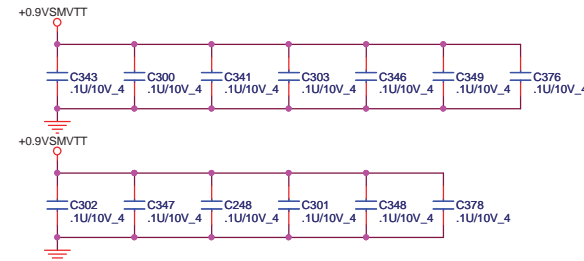
DDRII DUAL CHANNEL A,B.

DDRII A CHANNEL

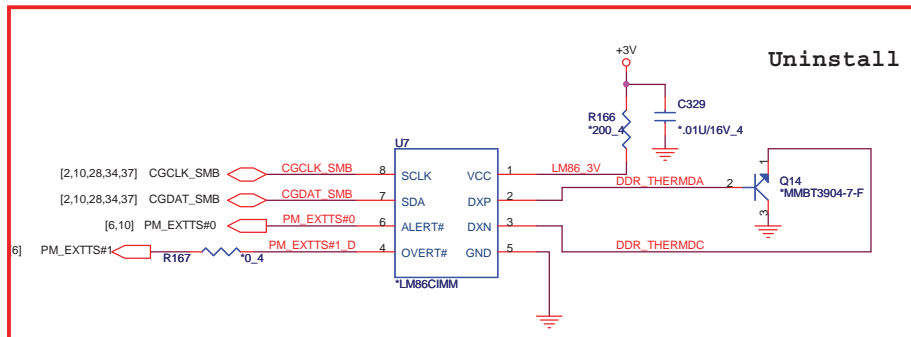


Layout note: Place one cap close to every 2 pullup resistors terminated to SMDR_VTERM

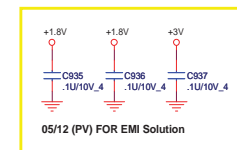
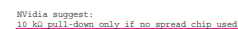
DDRII B CHANNEL



M_B_A[14..0] M_B_A[14..0] [7,10]
M_A_A[14..0] M_A_A[14..0] [7,10]










Date: Wednesday, August 06

SEE Datasheet for details on G9x Straps

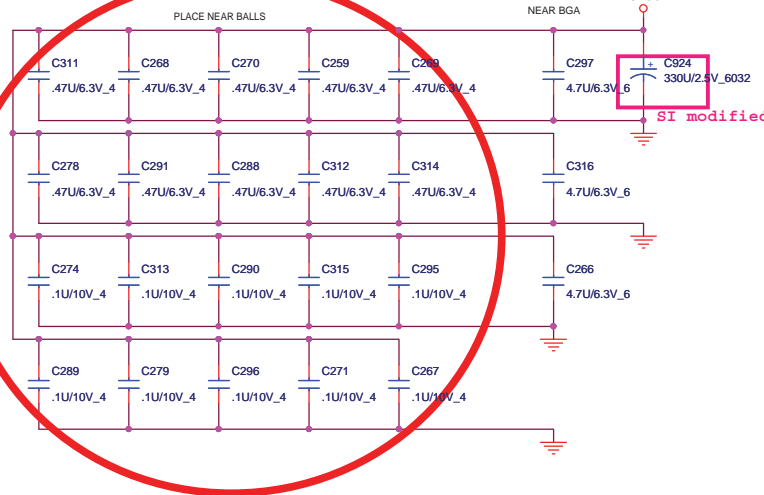
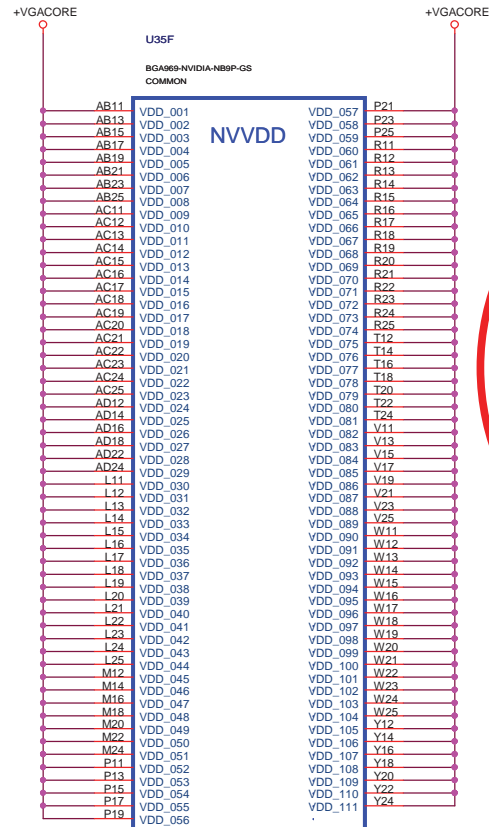
NVidia suggest:
Pull low 10KB for R95, R7 & R525

1000
0010
XXXX
XXXX
0001
1111

CS33572FR13 RES CHIP 35.7K 1/16W +/-1% (0402)

 NB5	PROJECT : UT6 Quanta Computer Inc.		
	Size C	Document Number NV9X (GPIO & STRRAPS) 4/5	Rev E3
Date: Wednesday, August 06, 2008 11:00 AM of 45			

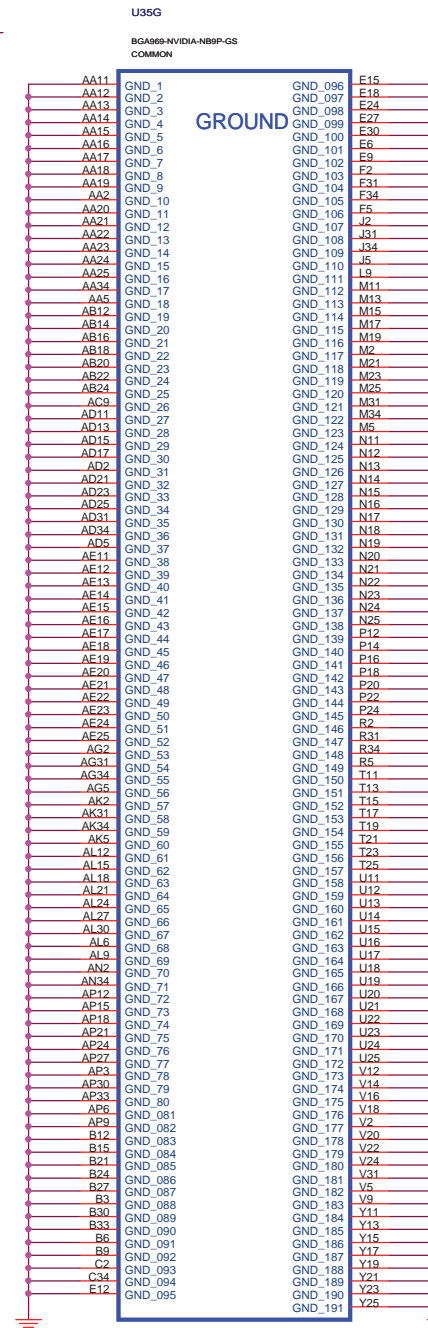
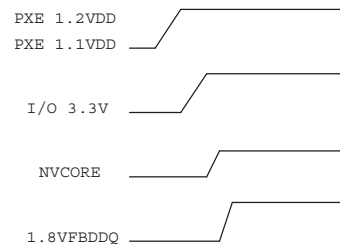
NVVDD Decoupling



Follow Design Guide DG-03276-001 4.7uFx3
and 0.47x10 uF instead of 0.1uF x10

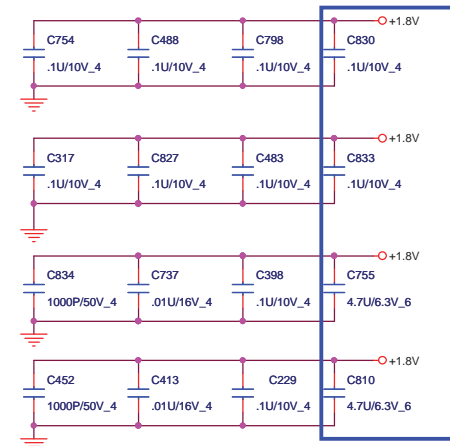
NB9M: VGACORE +0.90V (Normal) , +1.09V



power up sequence







PROJECT : UT6
Quanta Computer Inc.



Size	Document Number	Rev
Custom	NV9X (POWER & GND) 5/5	E3A
Date: Wednesday, August 06, 2008	Sheet 16 of 46	



[13] VMA_DQ[63..0]  

[13] VMA_DM[7..0]  

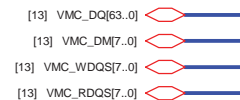
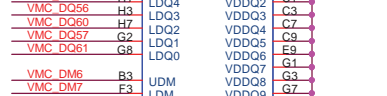
[13] VMA_WDQS[7..0]  

[13] VMA_RDQS[7..0]  

256Mb : AKD5JGAT^05
512Mb : AKD59G-T^01



<http://hobi-elektronika.net>

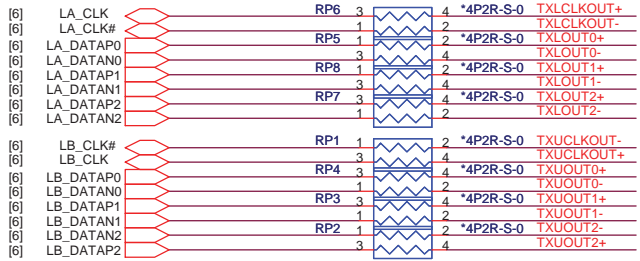


- 

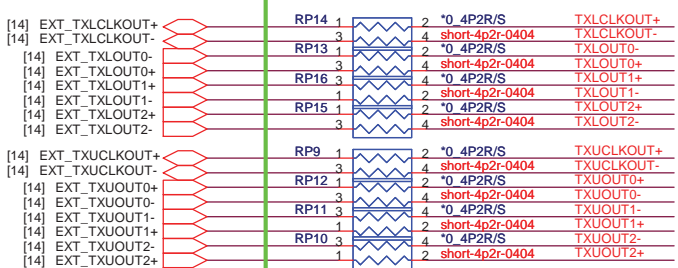
Size Custom	Document Number NV9X VRAM-2(GDDR2 BGA84)	Rev E3A
Date: Wednesday, August 06, 2008		Sheet 18 of 46

1. If LCD connector near GPU, then place these series Resistors near GPU
2. If LCD connector near N/B, then place these series Resistors near N/B

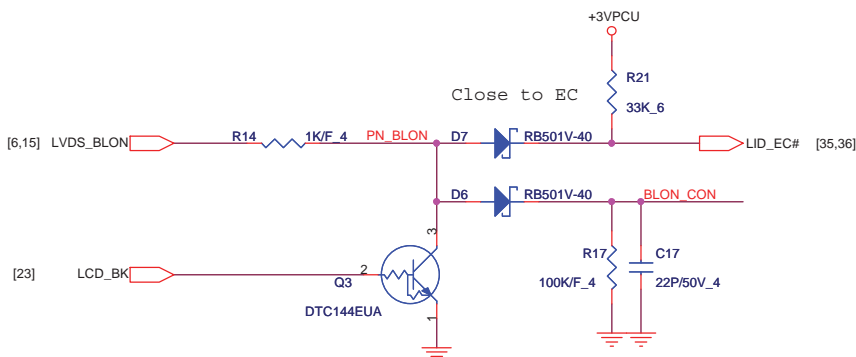
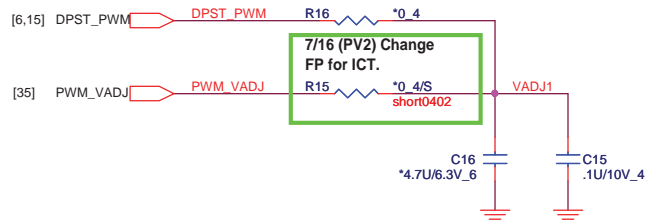
OPTION SIGNAL FROM NB FOR UMA VGA



OPTION SIGNAL FROM Nvidia to VGA



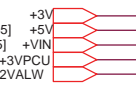
07/14 (PV2) Change footprint for PE require.



Nvidia suggest:
Pull low 10KR for R95, R7 & R525

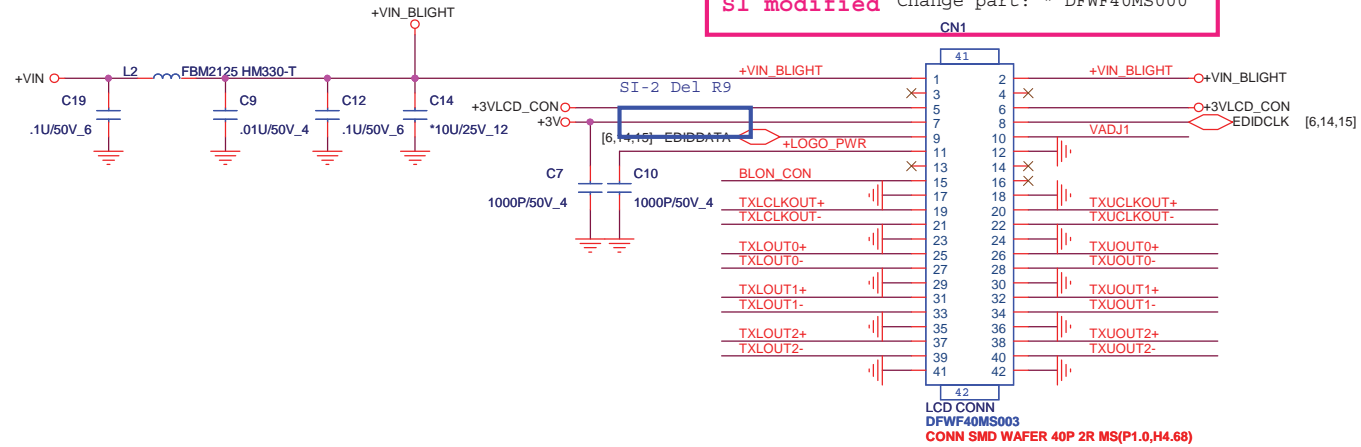


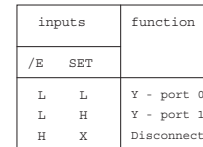
[2,4,6,9,10,11,12,14,15,20,21,22,23,24,25,26,27,28,29,30,31,32,34,35,37,38,42,43,45]
[3,20,24,25,27,28,30,31,34,36,38,45]
[29,38,39,40,41,42,43,44,45]
[21,30,31,35,36,38,39,40,45]
[27,30,36,40,45]



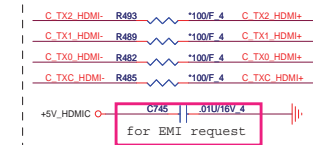
19

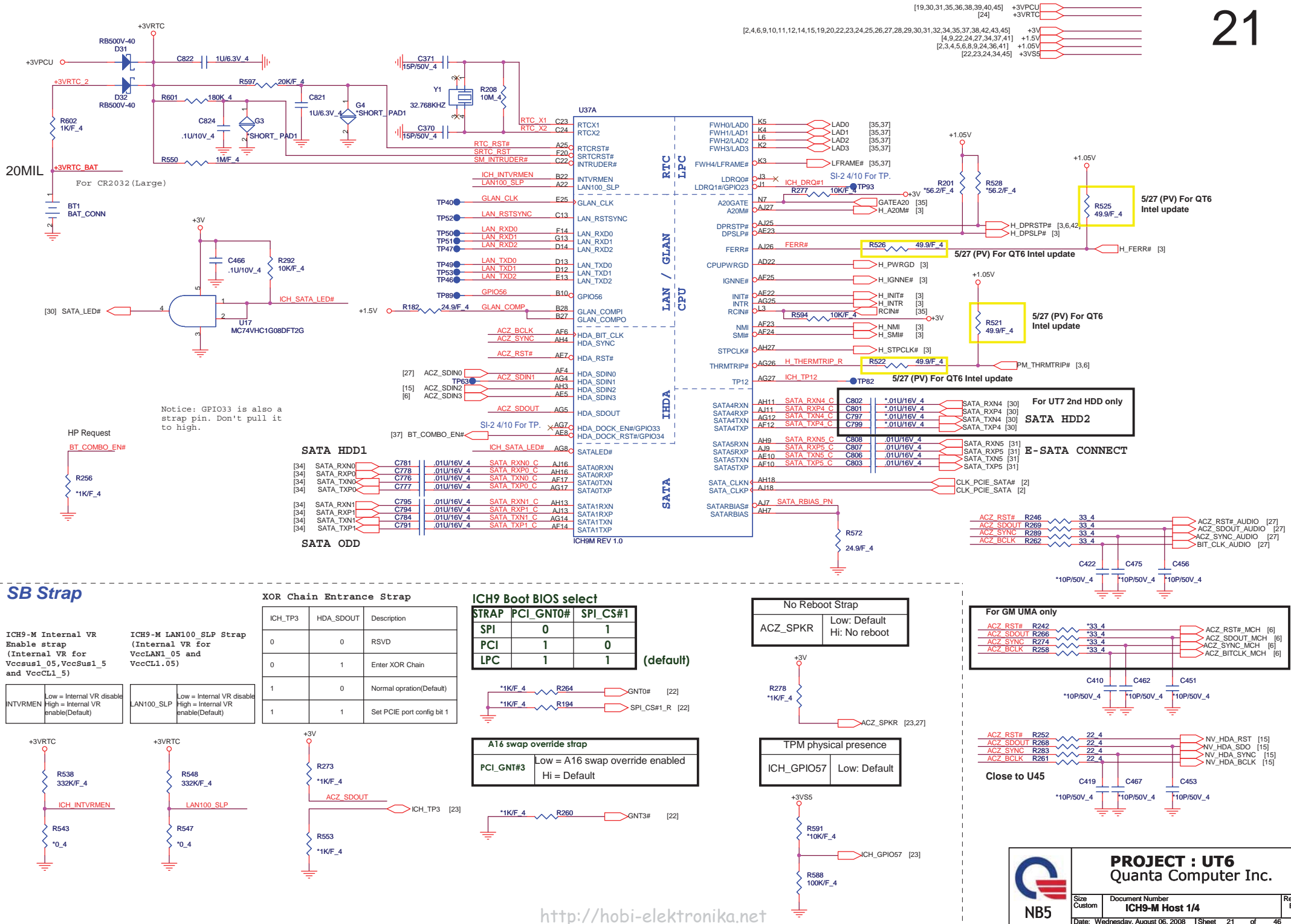
SI modified Change part: " DFWF40MS000





SI-2 change contact signal



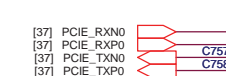


SWAP PCIE PORT6 to PORT2 (Lan and New card swap) -->Rename the port name by function and port

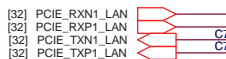
[2,4,6,9,10,11,12,14,15,19,20,21,23,24,25,26,27,28,29,30,31,32,34,35,37,38,42,43,45] +1.5V
[23,31,37,41,42,43,45] +3VSUS
[21,23,24,34,45] +3VS5

22

MINI CARD PCI-E(WLAN)

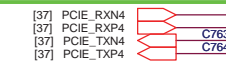


PCIE-LAN

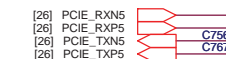


MINI CARD PCI-E (ROBSAN)

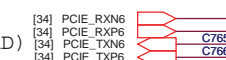
07/09 (PV2) Del C761,C762, add TP98, TP99 for no support ROBSON card.



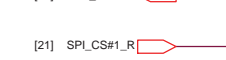
TV CARD PCI-E



FireWire PCI-E



EXPRESS CARD (NEW CARD)

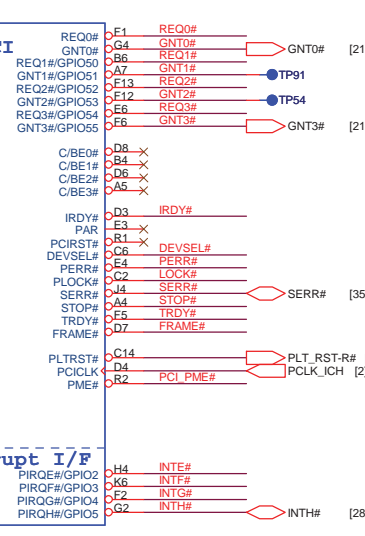


SI modified

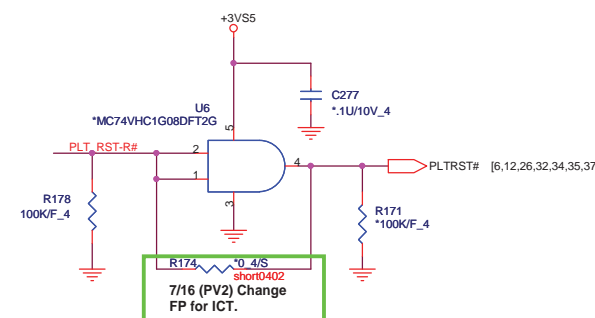
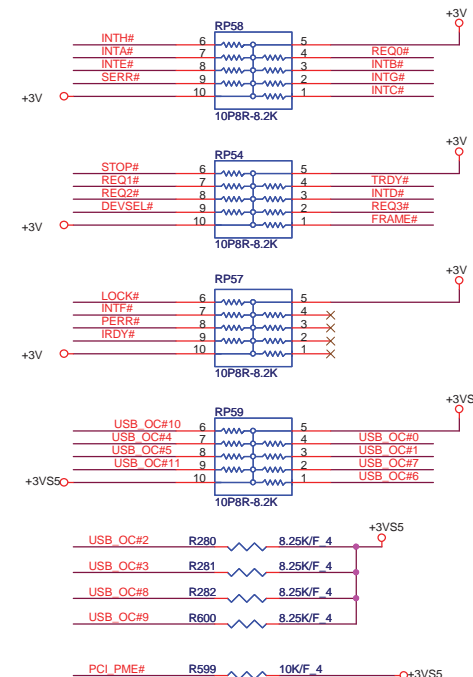
Del GM HDCP circuit

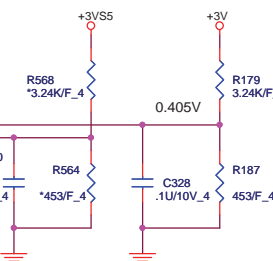
Del U16, R297, R285, R275, R291, R286, C476C478, R294

512K byte SPI ROM
For HDCP only
For GM HDCP

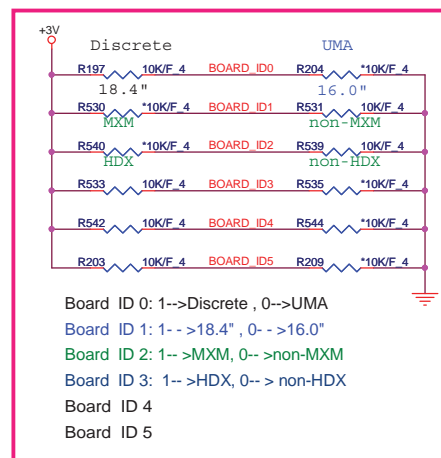


USB Connector
E-SATA and USB Connector
FINGERPRINT
Carama USB
Docking
BLUETOOTH
NEW CARD
USB Connector
USB Connector
Mini Card WLAN
Mini Card TV





SI modified Del T18,TP84,TP86



Board ID 0: 1-->Discrete, 0-->UMA
Board ID 1: 1-->18.4", 0-->16.0"
Board ID 2: 1-->MXM, 0-->non-MXM
Board ID 3: 1-->HDX, 0-->non-HDX
Board ID 4
Board ID 5

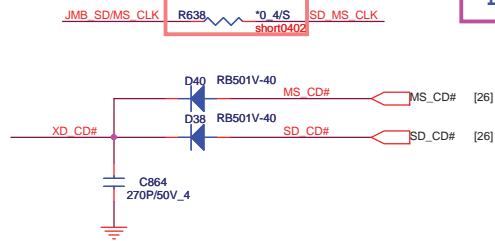


Size Custom	Document Number ICH9-M GPIO 3/4	Rev E3A
Date: Wednesday, August 06, 2008		Sheet 23 of 46

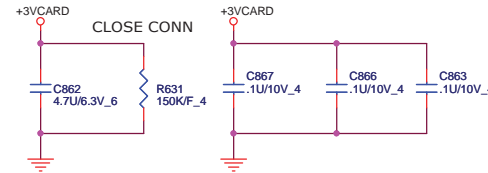


MV modify short pad

Delete JMB 385

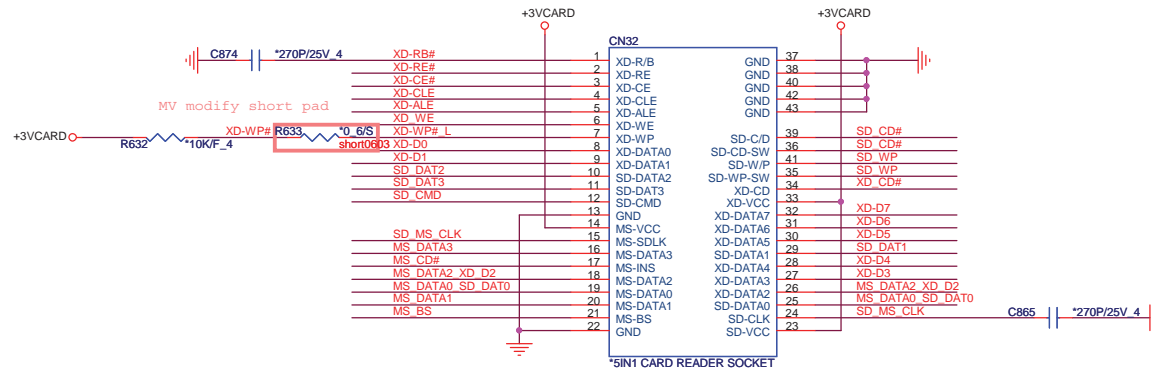


Close to CN34



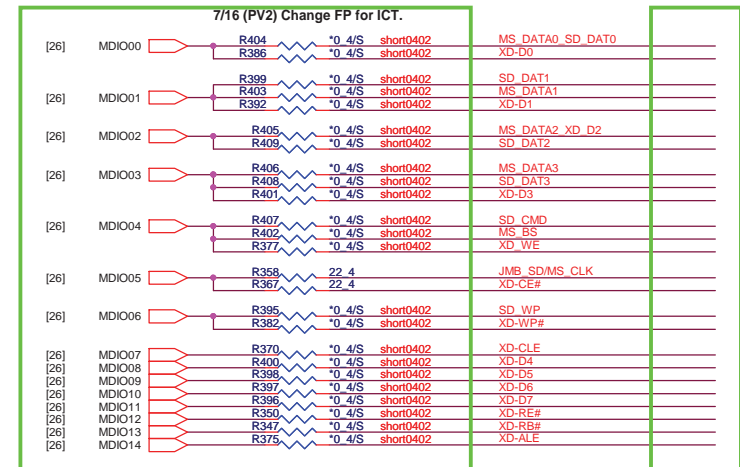
	SD/MMC	MS	XD
MDIO0	SD DAT0	MS D0	XD D0
MDIO1	SD DAT1	MS D1	XD D1
MDIO2	SD DAT2	MS D2	XD D2
MDIO3	SD DAT3	MS D3	XD D3
MDIO4	SD CMD	MS BS	XD WE#
MDIO5	SD CLK	MS SCLK	XD CE#
MDIO6	SD WP		XD WP#
MDIO7			XD CLE
MDIO8	SD DAT4		XD D4
MDIO9	SD DAT5		XD D5
MDIO10	SD DAT6		XD D6
MDIO11	SD DAT7		XD D7
MDIO12			XD RE#
MDIO13			XD RB#
MDIO14			XD ALE
CR1 LEDN	SD1 LED#	MS1 LED#	XD LED#
CR1 PCTLN	SD1 PCTL#	MS1 PCTL#	XD1 PCTL#
CR1 CD0	SD1 CD#		XD CV#
CR1 CD1		MS1 CD#	XD CD#

5 IN1 CARD READER XD, MMC/SD, MS/MSP

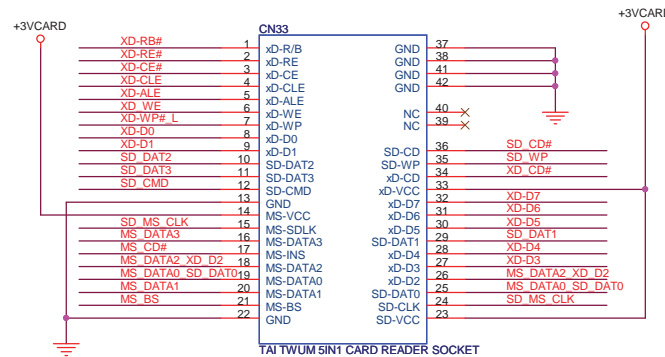


SI modified Footprint: "4in1-72700327123-43p-1"

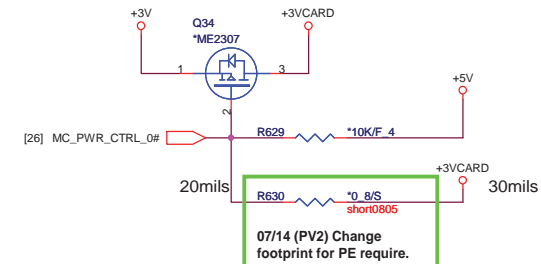
7/16 (PV2) Delete net for ICT.



2ND SOURCE

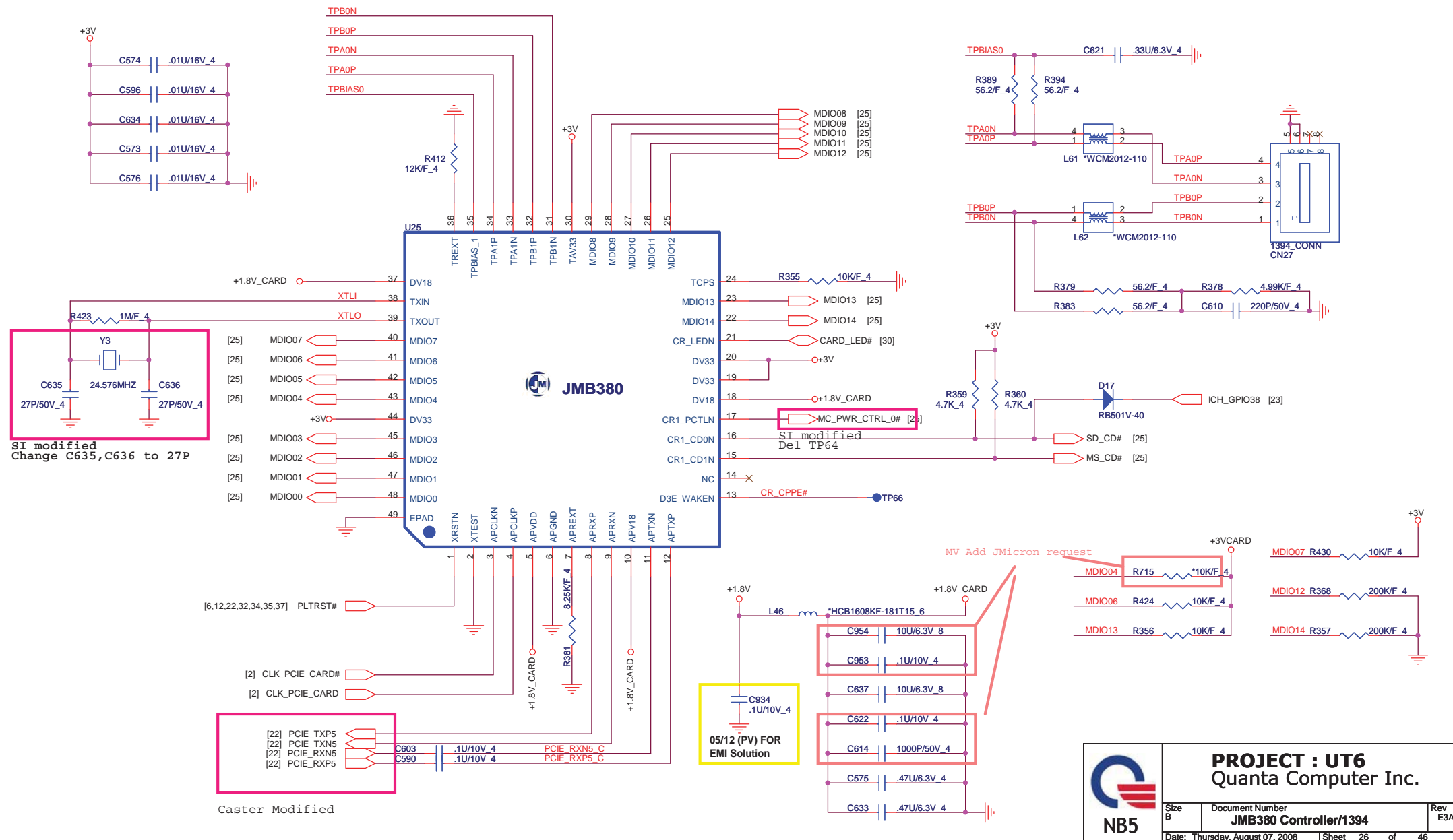


SI modified Footprint: "7IN1-R015-B11-LM-42P-L"



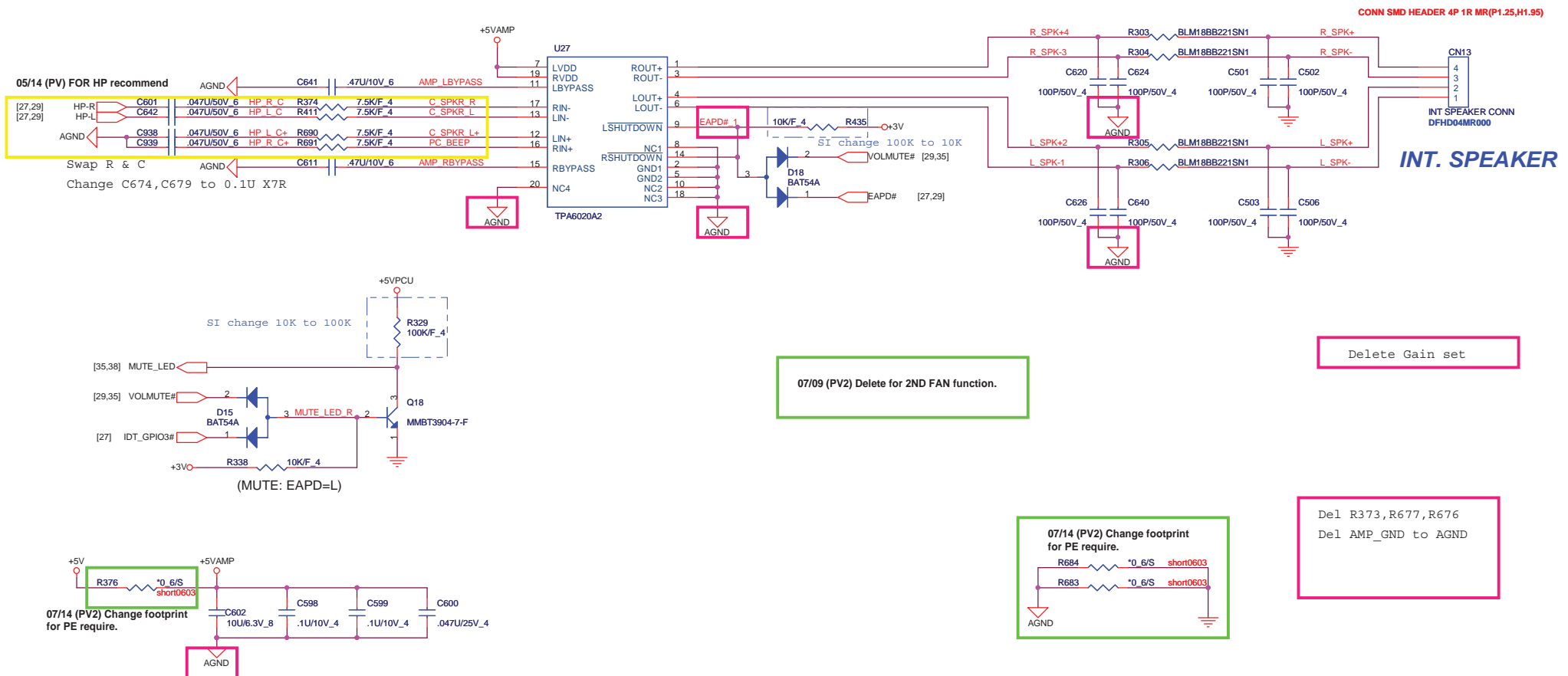
PROJECT : UT6
Quanta Computer Inc.

Size Custom Document Number CardReader SOCKET Rev E3A
Date: Wednesday, August 06, 2008 Sheet 25 of 46

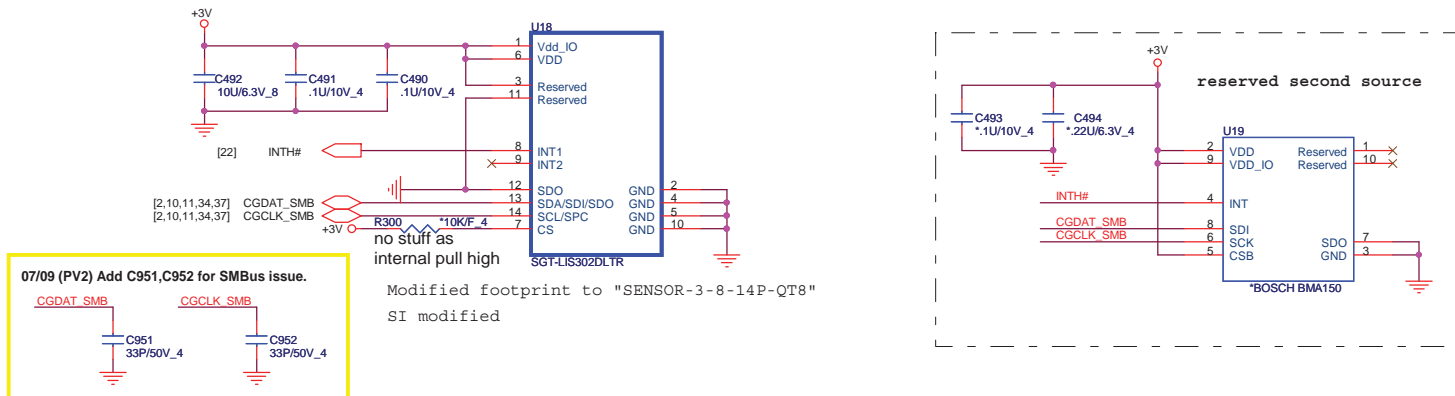


AUDIO AMPLIFIER

28

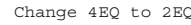
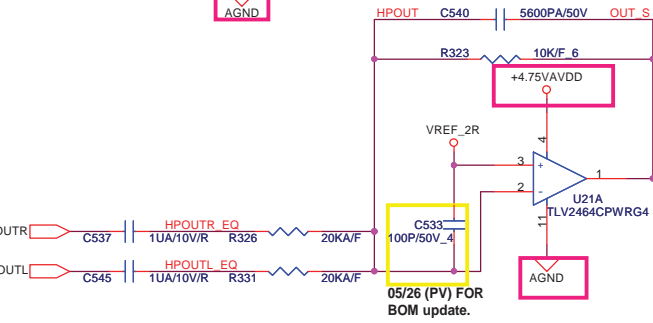


Accelerometer Sensor

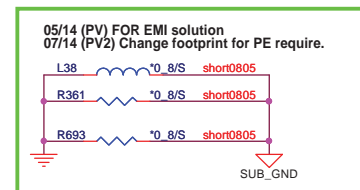
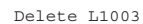


PROJECT : UT6
 Quanta Computer Inc.

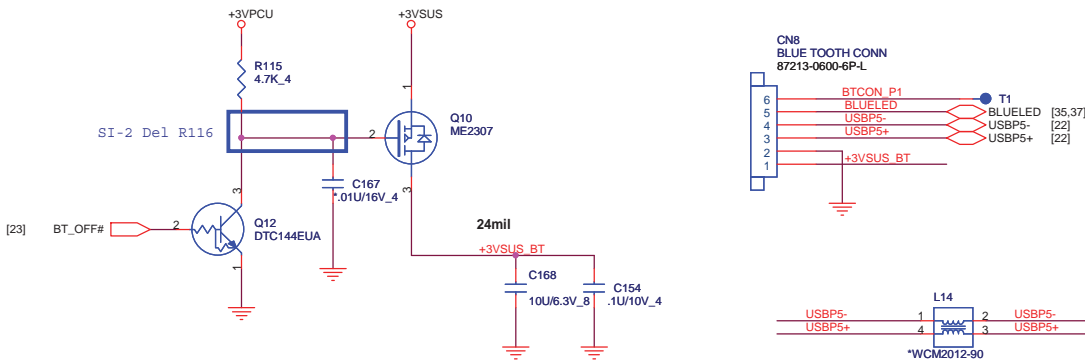
Size Custom	Document Number AMP_TPA6017/Accelerometer	Rev E3A
Date: Wednesday, August 06, 2008	Sheet 28	of 46



MODEL	UT6	UT7
R316	60.4K/F_6	40.2K/F_6
R319	60.4K/F_6	40.2K/F_6
R330	60.4K/F_6	80.6K/F_6
R314	60.4K/F_6	80.6K/F_6
C509	0.027U/25V_6	0.022U/50V_6
C510	0.027U/25V_6	0.022U/50V_6
C529	0.027U/25V_6	0.039U/16V_6
C543	0.027U/25V_6	0.039U/16V_6

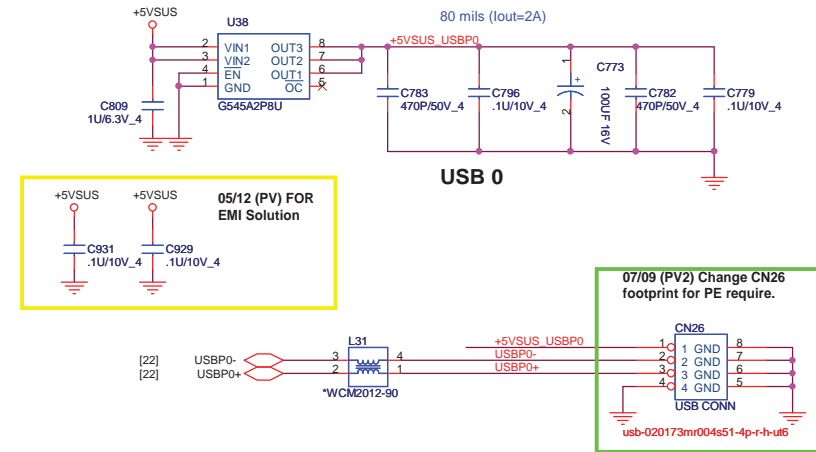


BLUETOOTH

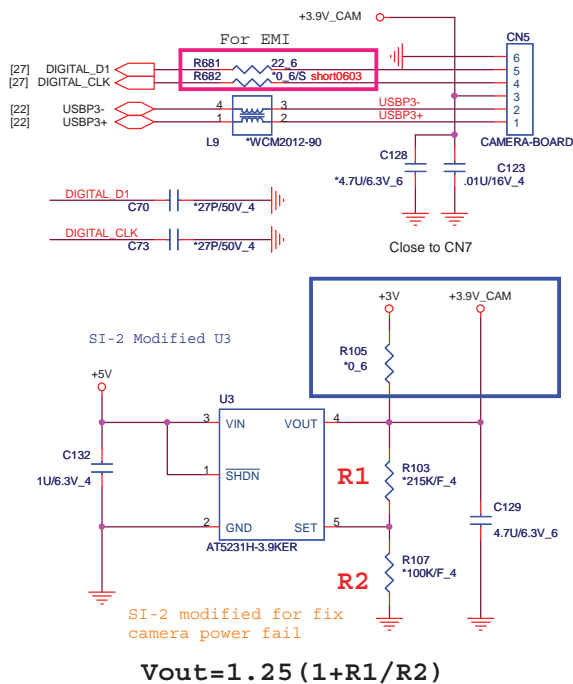


USBX1 and E-SATA

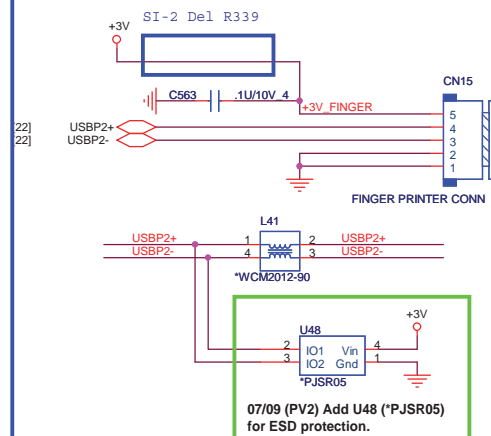
31



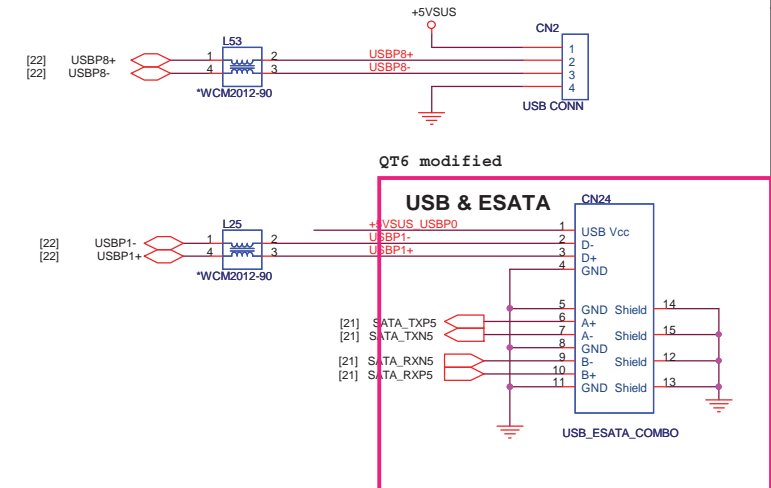
USB CAMERA /DIGITAL MIC CONNECT



USB fingerprint CON

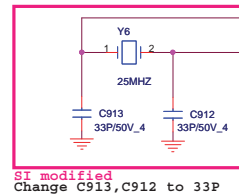
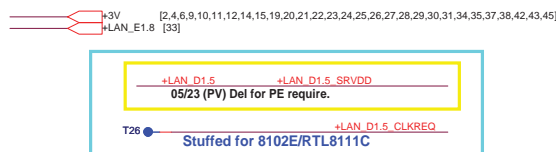


1. ESD GND
2. SYSTEM GND
3. USB-
4. USB+
5. USB PWR(+3V)

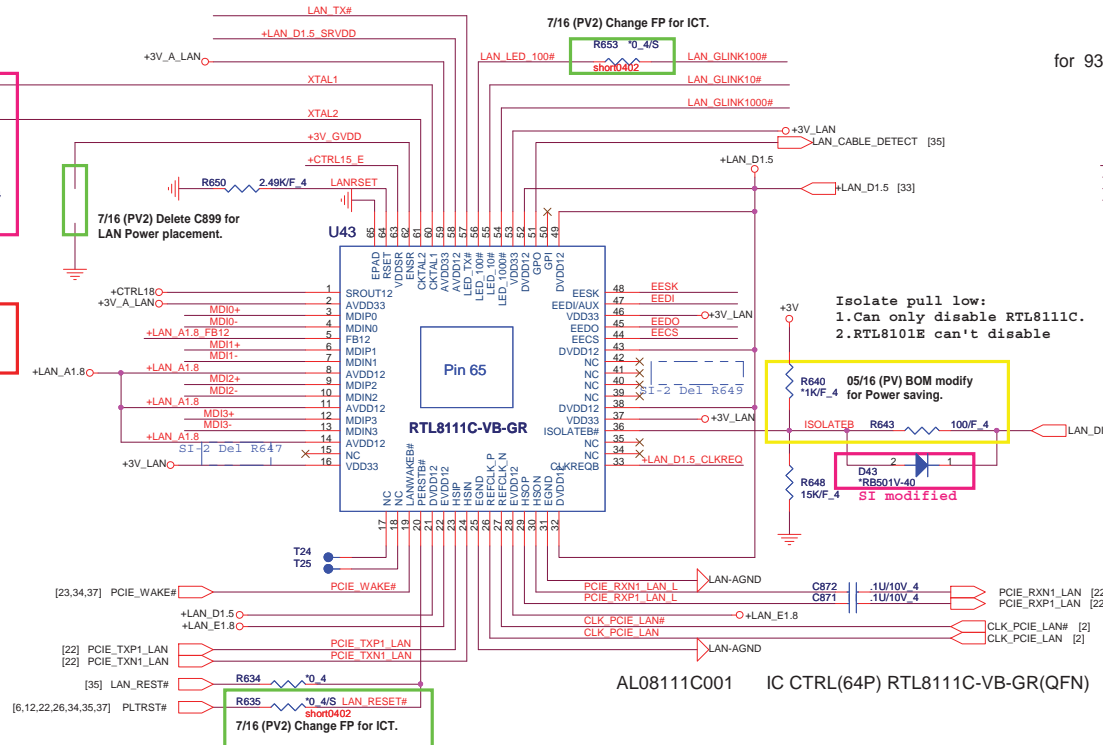


	PROJECT : UT6 Quanta Computer Inc.		Rev E3A	
	Size Custom	Document Number BT/WC/FT/TS/ESATA/USB		
	Date: Wednesday, August 06, 2008	Sheet 31 of 46		

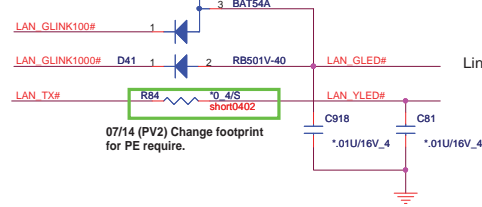
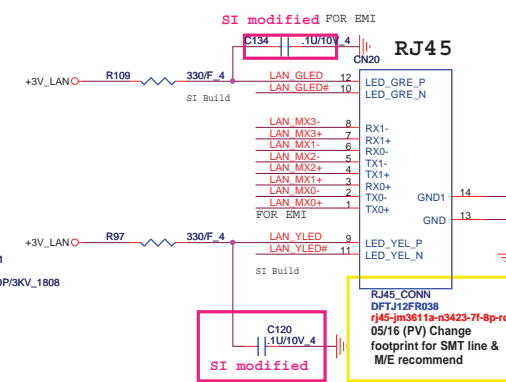
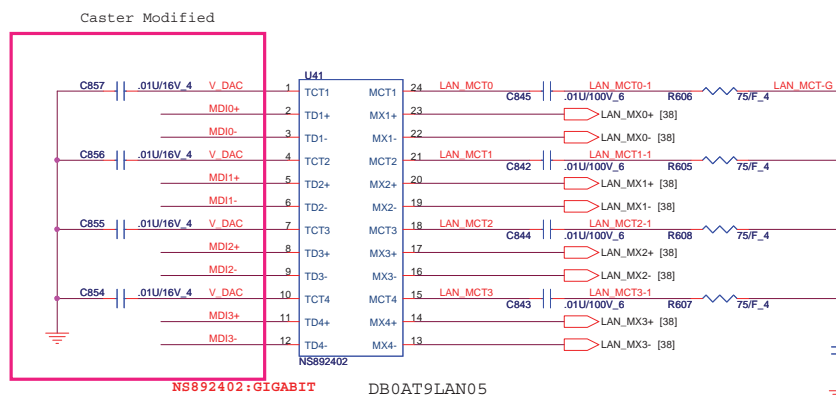
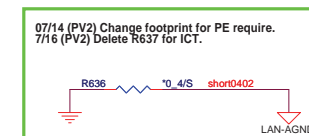
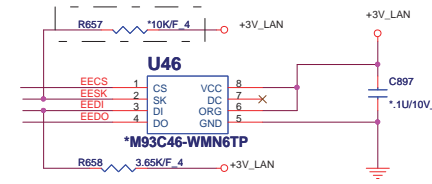
T : Stuffed for RTL8111C(10/100/1000)



U18#63 wider than 40 mils
U18#1 wider than 60 mils

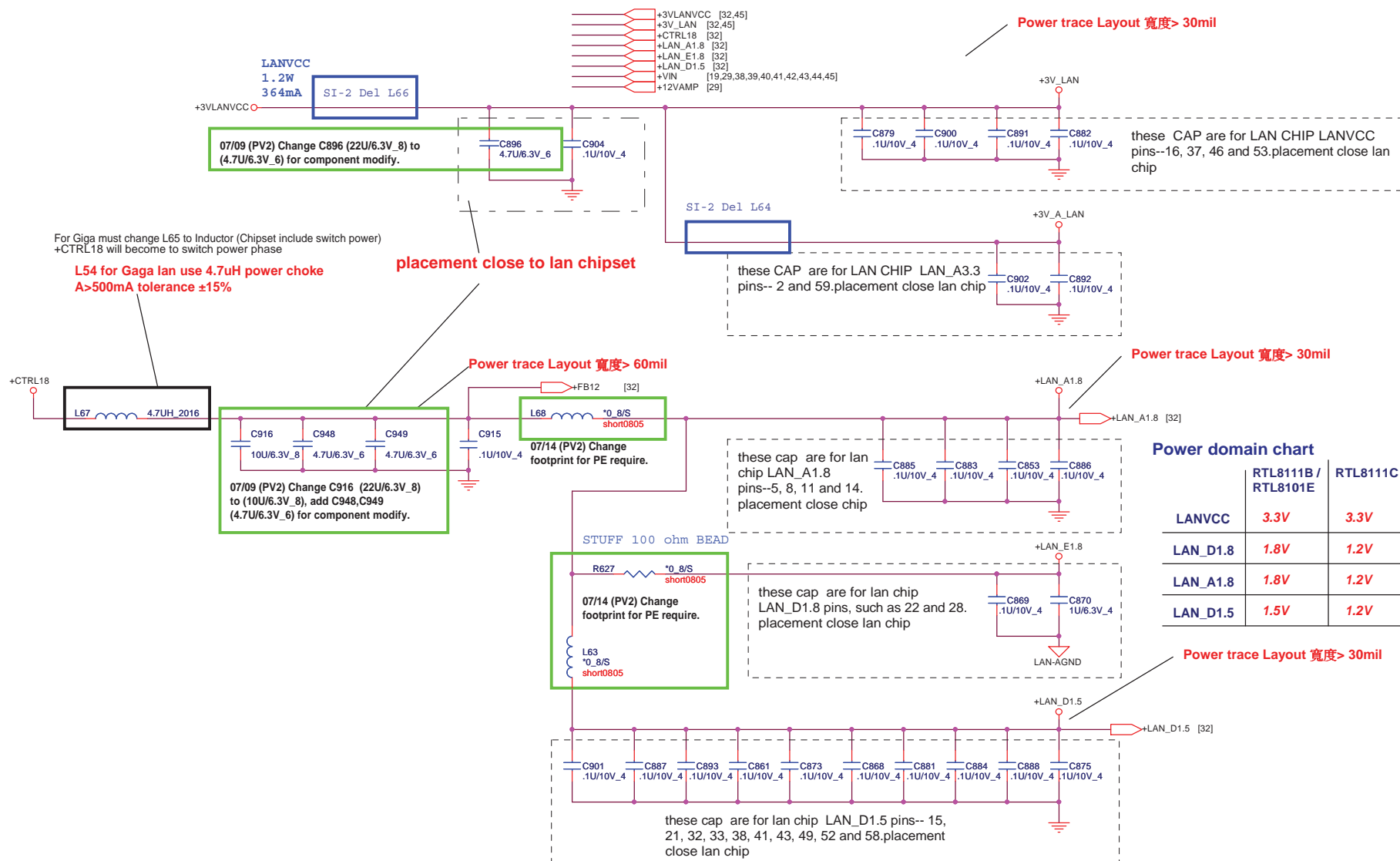


for 93C56 used. NC if 93C46 is used.



PROJECT : UT6
Quanta Computer Inc.

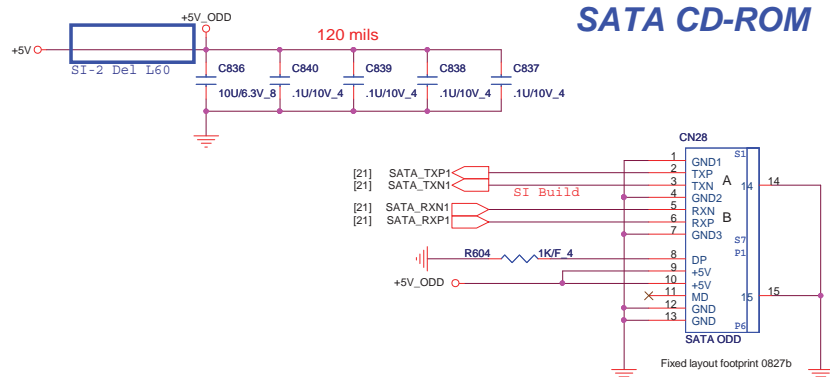
Size	Document Number	Rev
Custom	RTL8111C & RJ45	E3A
Date: Wednesday, August 06, 2008	Sheet 32 of 46	



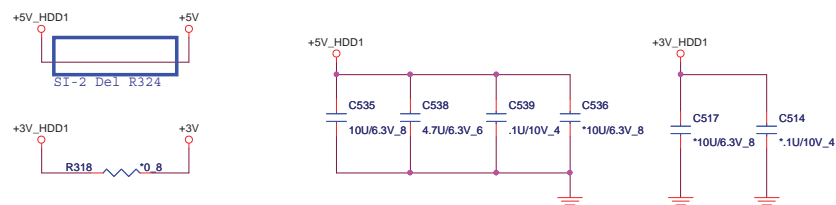
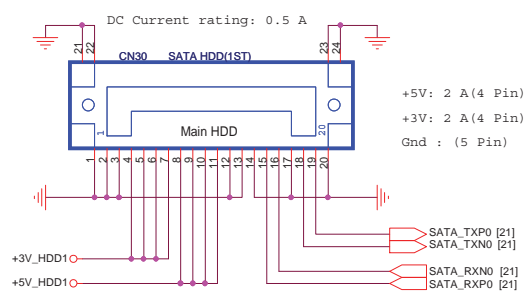
PROJECT : UT6
Quanta Computer Inc.

Size A3	Document Number LAN Power	Rev E3A
Date: Wednesday, August 06, 2008	Sheet 33 of 46	

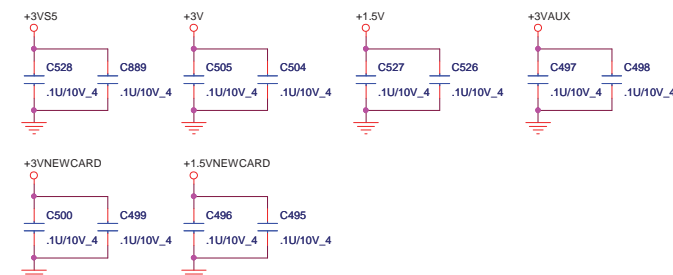
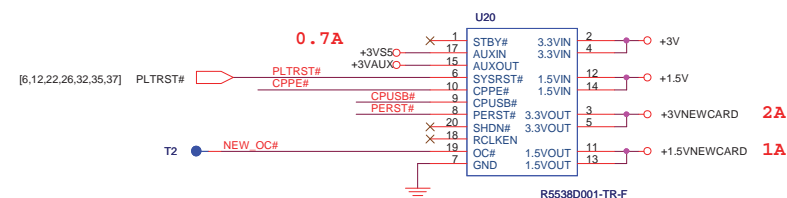
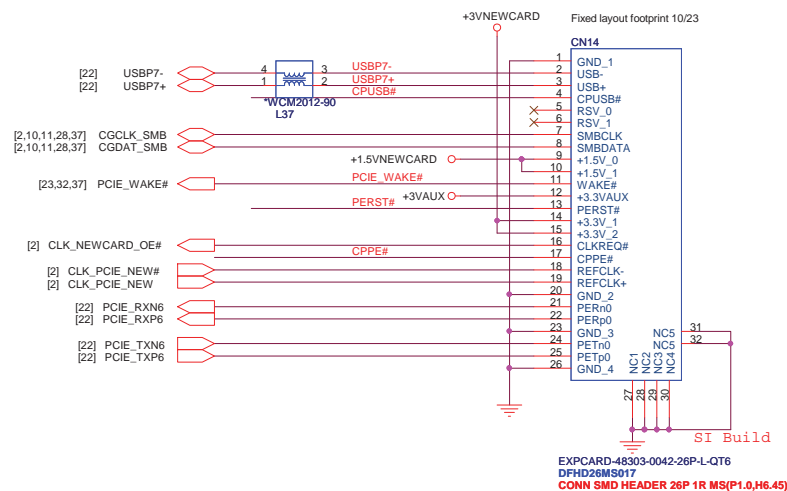
SATA CD-ROM



SATA_1 CONNECTOR

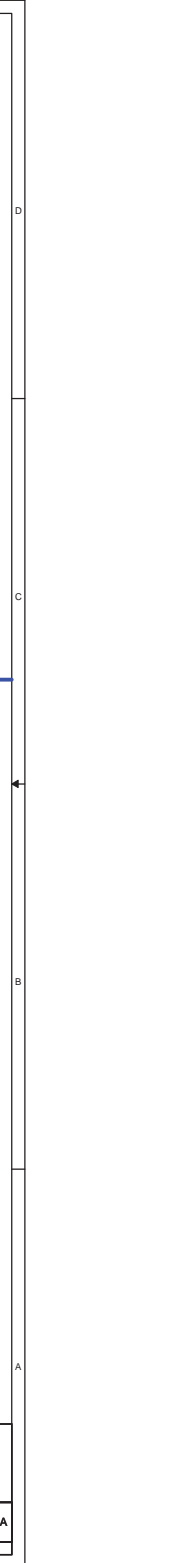
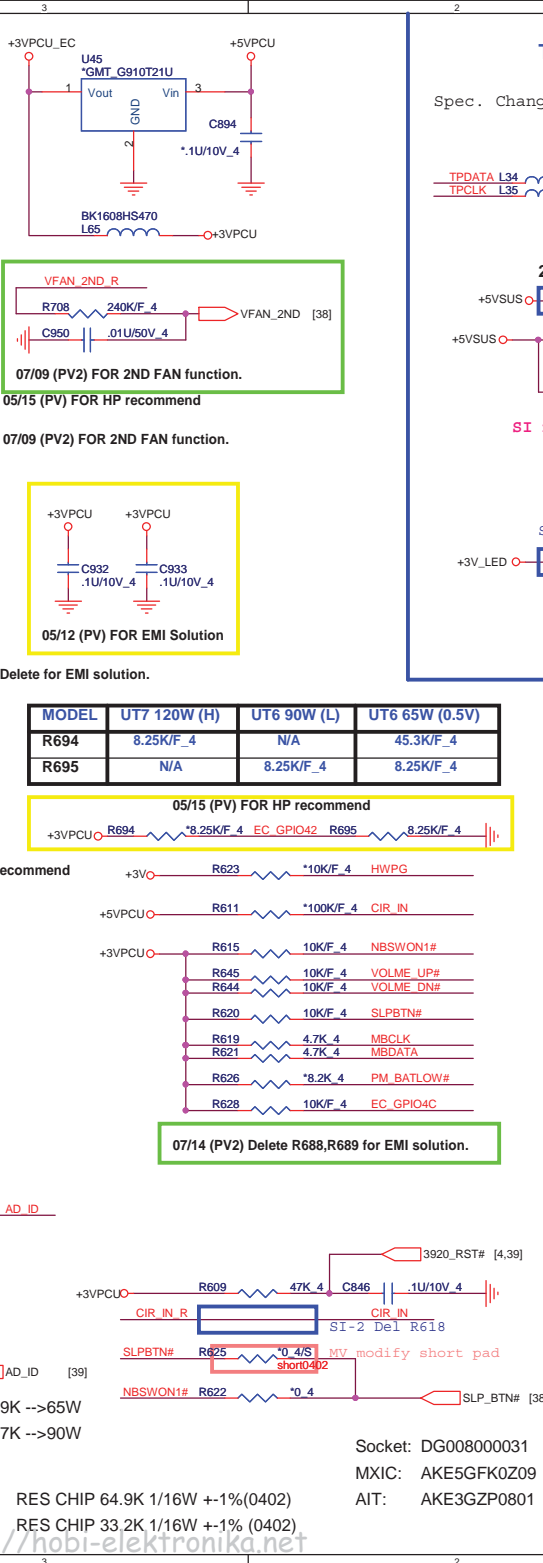


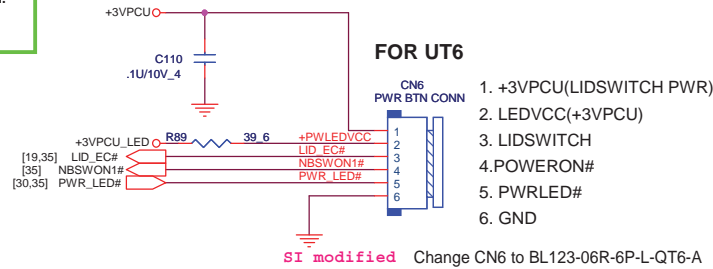
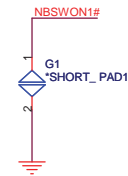
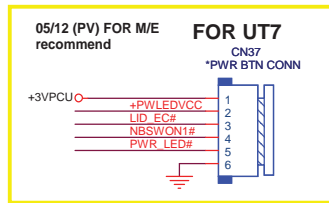
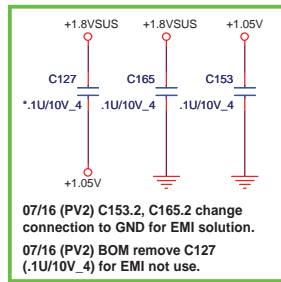
NEWCARD (PCIEXPRESS*1 + USB*1)



PROJECT : UT6
Quanta Computer Inc.

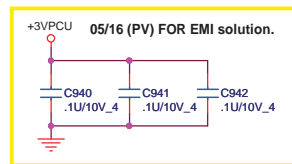
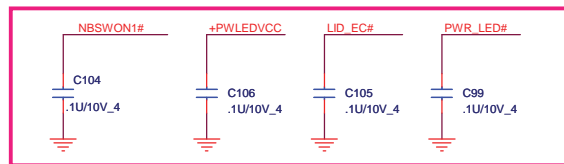
Size Custom	Document Number ODD/HDD/NEW CARD	Rev E3A
Date: Wednesday, August 06, 2008		Sheet 34 of 46





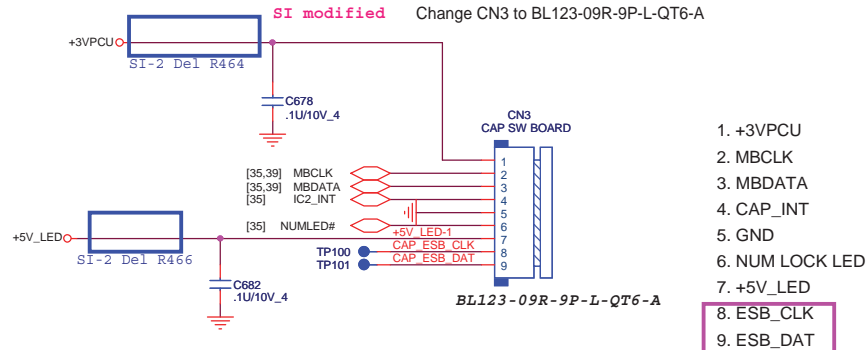
POWER BOTTOM CONNECT

SI modified For EMI

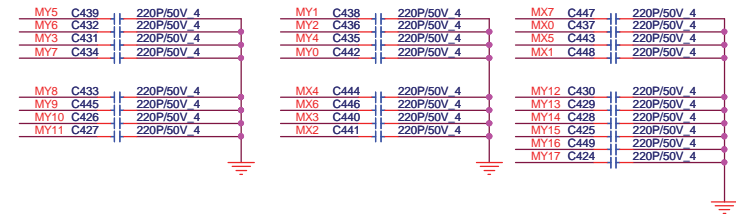
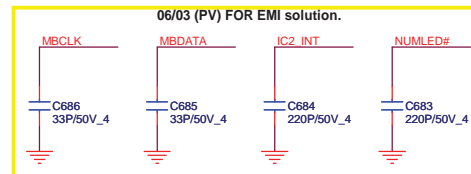


CAP SW CONNECT

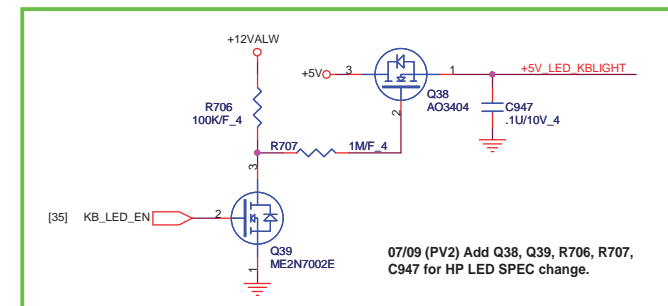
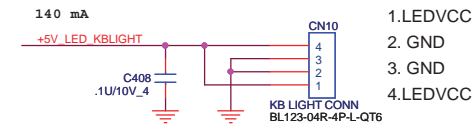
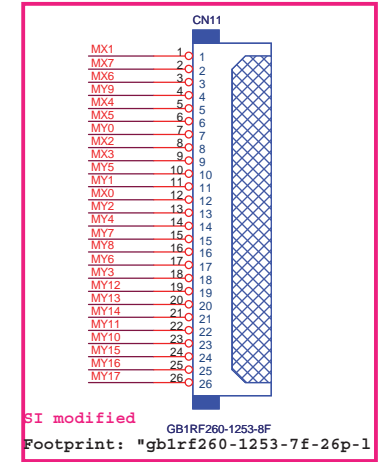
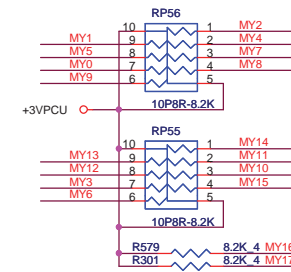
Change CN3 to BL123-09R-9P-L-QT6-A



07/14 (PV2) Delete L69, L70, C922, C923 for EMI solution.



KEYBOARD PULL-UP

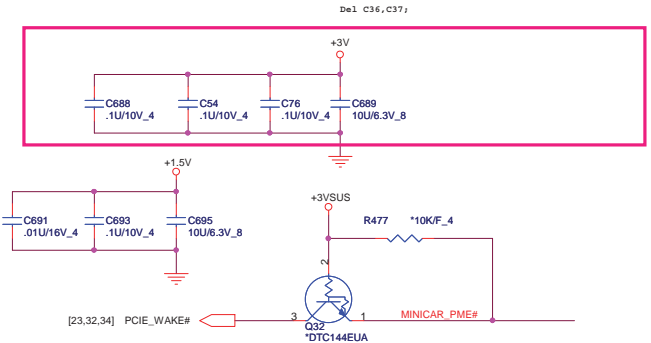
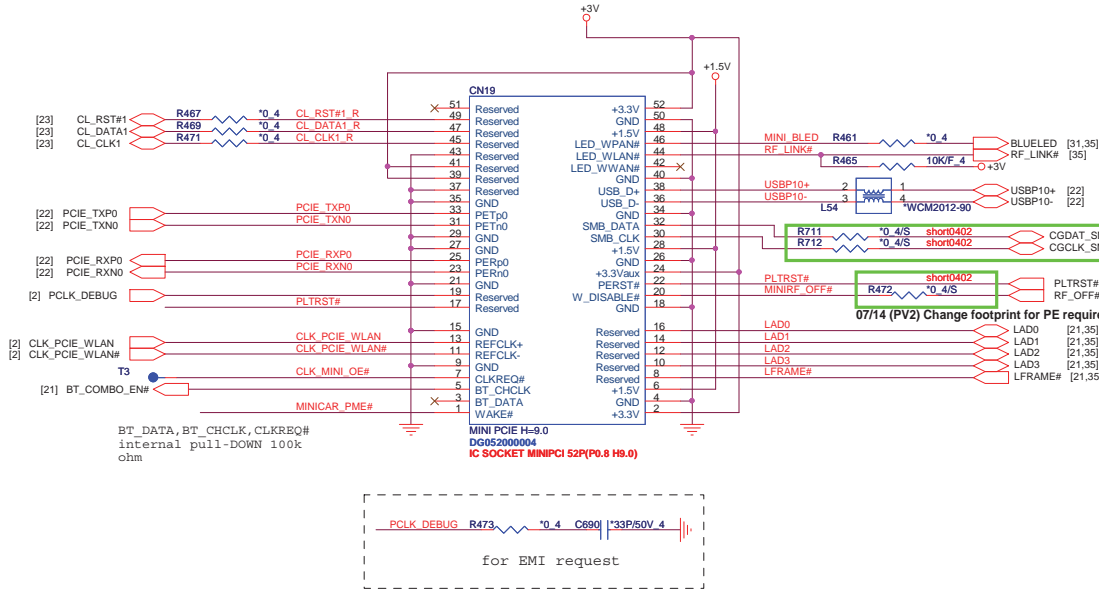


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Quanta Computer Inc.

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Custom	KB/CAP/POWER CONN	E3A
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Mini PCI-E Card 1 WLAN

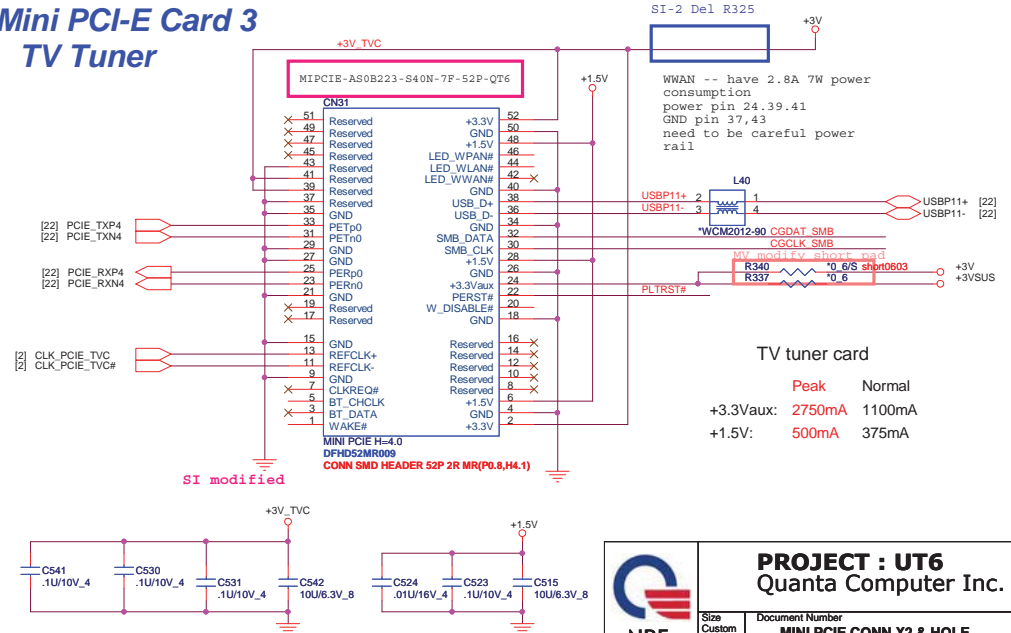
Delete R110,R78
+3V must have a 120mil plane
Each pin 25mil



Mini PCI-E Card 2 ROBSON

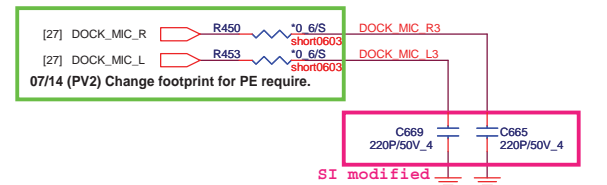
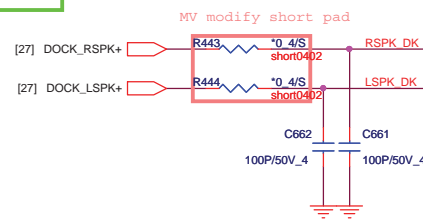
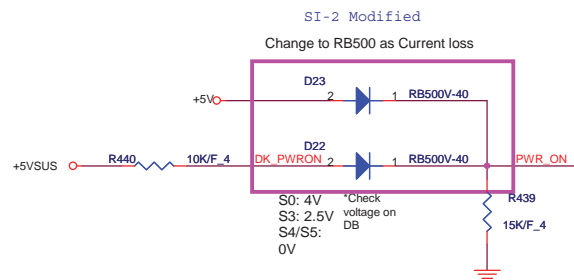
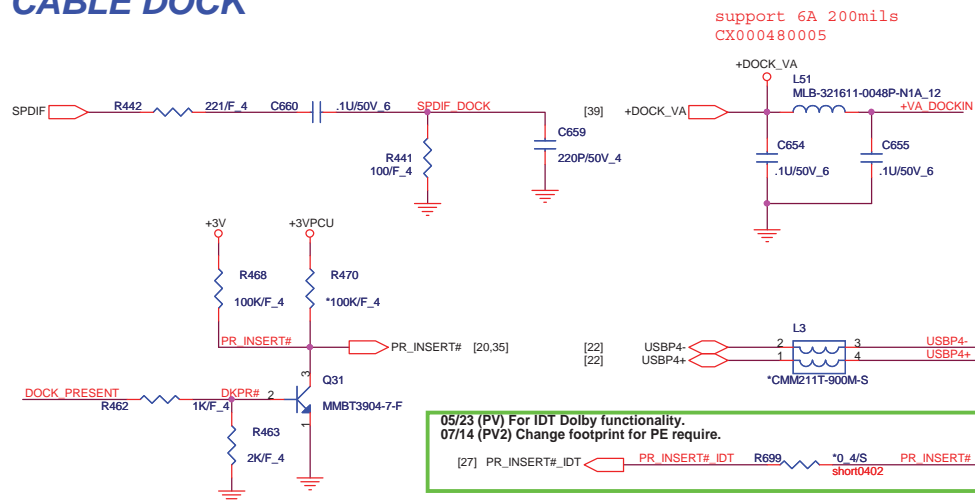
07/09 (PV2) Delete for no support ROBSON card.

Mini PCI-E Card 3 TV Tuner



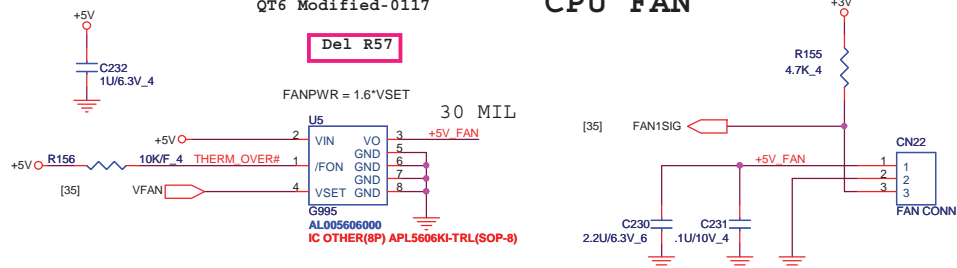
PROJECT : UT6
Quanta Computer Inc.

Size Custom	Document Number MINI PCIE CONN X2 & HOLE	Rev E3A
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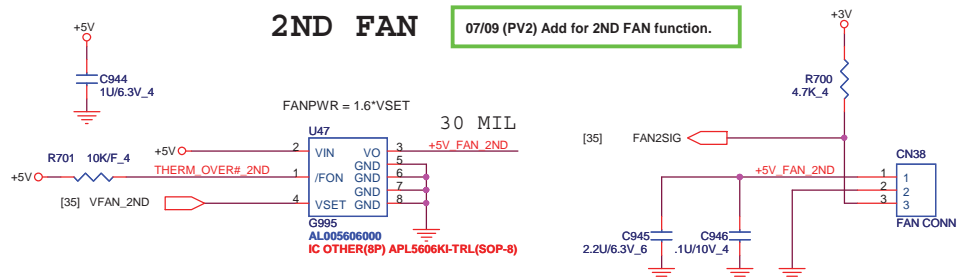
QT6 Modified-0117

CPU FAN

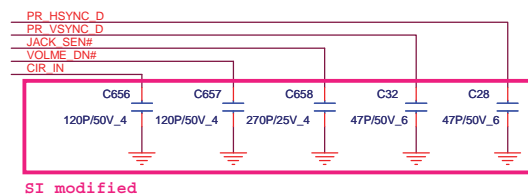
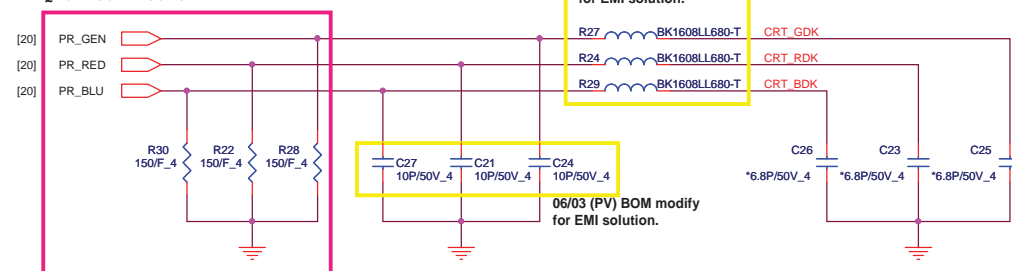


2ND FAN

07/09 (PV2) Add for 2ND FAN function.

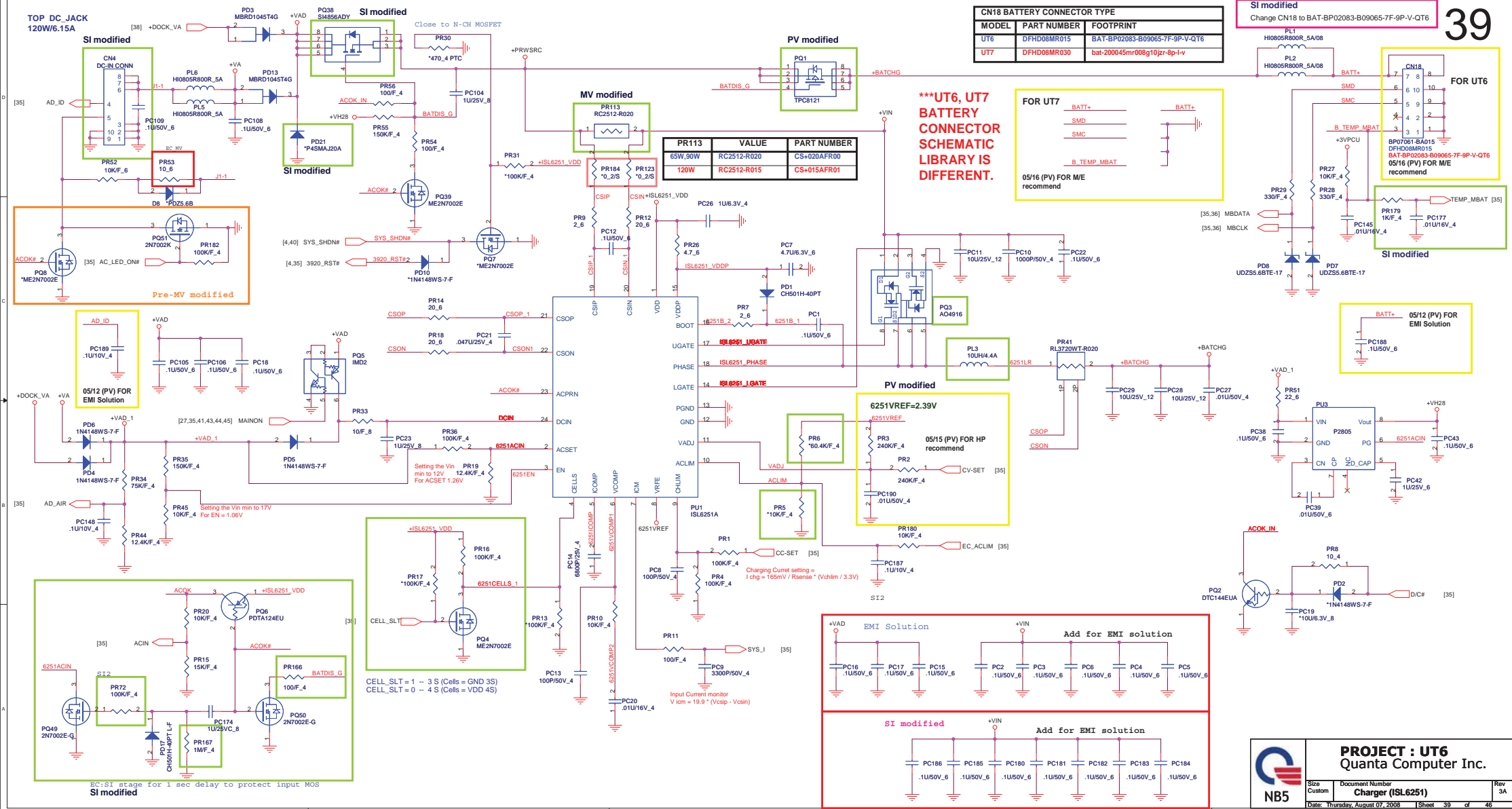


QT6 modified-0117



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40

DC/DC +3V_ALW/+5V_ALW/+5V_ALW2 /+12V_ALW

Place these CAPs close to FETs

Pre-MV modified +VIN

Place these CAPs close to FETs

5 Volt +/- 5%
Countinue current:5A
Peak current:7.5A
OCP minimum 10A

3.3 Volt +/- 5%
Countinue current:5A
Peak current:7.5A
OCP minimum 9A

40

DC/DC +3V_ALW/+5V_ALW/+5V_ALW2 /+12V_ALW

Place these CAPs close to FETs

Pre-MV modified +VIN

Place these CAPs close to FETs

5 Volt +/- 5%
Countinue current:5A
Peak current:7.5A
OCP minimum 10A

3.3 Volt +/- 5%
Countinue current:5A
Peak current:7.5A
OCP minimum 9A

40

DC/DC +3V_ALW/+5V_ALW/+5V_ALW2 /+12V_ALW

Place these CAPs close to FETs

Pre-MV modified +VIN

Place these CAPs close to FETs

5 Volt +/- 5%
Countinue current:5A
Peak current:7.5A
OCP minimum 10A

3.3 Volt +/- 5%
Countinue current:5A
Peak current:7.5A
OCP minimum 9A

PC93 10U/25V_12

PC87 1000P/50V_4

PC88 .1U/50V_6

DEL PR78 0_6

PC127 .1U/50V_6

PC65 1U/6.3V_4

PC126 .1U/10V_4

PC122 1U/6.3V_4

PC66 4.7U/25V_8

DEL PR72 (47ohm_6)

+5V_VCC1

PC79 .1U/50V_6

PC76 1000P/50V_4

PC83 10U/25V_12

PC136 330U/6.3V_6X5.8

PR83 *0_4

PR84 *0_4/S

PC135 .1U/10V_4

PL10 2.5uH/7.5A

PR149 *2.2_8

PC137 *1500P/50V_4

PC20 FDS8884

5V LX

5V DH

5V DL

PC132 *1500P/50V_4

PC133 .1U/10V_4

PC134 330U/6.3V_6X5.8

PR85 *0_4/S

PR87 *0_4

PR148 *2.2_8

PC132 *1500P/50V_4

PL9 2.5uH/7.5A

+3V_VCC1

3V LX

3V DH

3V DL

PR137 309K/F_4

PGOOD1

PGOOD2

PR143 4.7_6

PC72 1U/50V_6

PR144 *SHORT-1A

PC81 1U/6.3V_4

PC77 .01U/50V_6

PC78 .01U/50V_6

PC82 .1U/25V_8

PR93 100K/F_4

PC85 2.2U/50V_8

PC88 .1U/50V_6

PC87 1000P/50V_4

PC93 10U/25V_12

PR83 *0_4

PR84 *0_4/S

PC135 .1U/10V_4

PL10 2.5uH/7.5A

PR149 *2.2_8

PC137 *1500P/50V_4

PC20 FDS8884

5V LX

5V DH

5V DL

PC132 *1500P/50V_4

PC133 .1U/10V_4

PC134 330U/6.3V_6X5.8

PR85 *0_4/S

PR87 *0_4

PR148 *2.2_8

PC132 *1500P/50V_4

PL9 2.5uH/7.5A

+3V_VCC1

3V LX

3V DH

3V DL

PR137 309K/F_4

PGOOD1

PGOOD2

PR143 4.7_6

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PC88 .1U/50V_6

PC87 1000P/50V_4

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PR87 *0_4

PR148 *2.2_8

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PR93 100K/F_4

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PR83 *0_4

PR84 *0_4/S

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PL10 2.5uH/7.5A

PR149 *2.2_8

PC137 *1500P/50V_4

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5V LX

5V DH

5V DL

PC132 *1500P/50V_4

PC133 .1U/10V_4

PC134 330U/6.3V_6X5.8

PR85 *0_4/S

PR87 *0_4

PR148 *2.2_8

PC132 *1500P/50V_4

PL9 2.5uH/7.5A

+3V_VCC1

3V LX

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PR137 309K/F_4

PGOOD1

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PR143 4.7_6

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PR144 *SHORT-1A

PC81 1U/6.3V_4

PC77 .01U/50V_6

PC78 .01U/50V_6

PC82 .1U/25V_8

PR93 100K/F_4

PC85 2.2U/50V_8

PC88 .1U/50V_6

PC87 1000P/50V_4

PC93 10U/25V_12

PR83 *0_4

PR84 *0_4/S

PC135 .1U/10V_4

PL10 2.5uH/7.5A

PR149 *2.2_8

PC137 *1500P/50V_4

PC20 FDS8884

5V LX

5V DH

5V DL

PC132 *1500P/50V_4

PC133 .1U/10V_4

PC134 330U/6.3V_6X5.8

PR85 *0_4/S

PR87 *0_4

PR148 *2.2_8

PC132 *1500P/50V_4

PL9 2.5uH/7.5A

+3V_VCC1

3V LX

3V DH

3V DL

PR137 309K/F_4

PGOOD1

PGOOD2

PR143 4.7_6

PC72 1U/50V_6

PR144 *SHORT-1A

PC81 1U/6.3V_4

PC77 .01U/50V_6

PC78 .01U/50V_6

PC82 .1U/25V_8

PR93 100K/F_4

PC85 2.2U/50V_8

PC88 .1U/5

40

DC/DC +3V_ALW/+5V_ALW/+5V_ALW2 /+12V_ALW

Place these CAPs close to FETs

Pre-MV modified +VIN

Place these CAPs close to FETs

5 Volt +/- 5%
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Peak current:7.5A
OCP minimum 10A

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PC136 330U/6.3V_6X5.8

PR83 *0_4

PR84 *0_4/S

PC135 .1U/10V_4

PL10 2.5uH/7.5A

PR149 *2.2_8

PC137 *1500P/50V_4

PQ20 FDS8884

5V LX

5V DH

5V DL

PQ24 SH4686DY-T1-E3

Rds(on) 20m ohm

PR136 232K/F_4

PGOOD1

PC80 .1U/50V_6

PR96 4.7_6

PC77 .01U/50V_6

PD11

BAT54SPT

PD12

BAT54SPT

PC82 .1U/25V_8

PR93 100K/F_4

+5VALW

+12VALW

PC85 2.2U/50V_8

PC81 1U/6.3V_4

PR144 *SHORT-1A

PR137 309K/F_4

+5V_VCC1

REFIN2

ILIM2

OUT2

SKIP

PGOOD2

ON2

DH2

LX2

PC72 1U/50V_6

PR143 4.7_6

3V LX

3V DH

3V DL

PQ16 FDS8884

PL9 2.5uH/7.5A

+3.3V_ALWP

PR85 *0_4/S

PC133 .1U/10V_4

PC134 330U/6.3V_6X5.8

PC132 *1500P/50V_4

PR148 *2.2_8

PQ19 FDS4410A

Rds(on) 20m ohm

PR87 *0_4

PGOOD2

PGOOD1

PR91 *0_4/S

HWPG [35,41,43,44]

[4,39] SYS_SHDN#

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NB5

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+5V/+3V (ISL6237)

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<http://hobi-elektronika.net>

40

DC/DC +3V_ALW/+5V_ALW/+5V_ALW2 /+12V_ALW

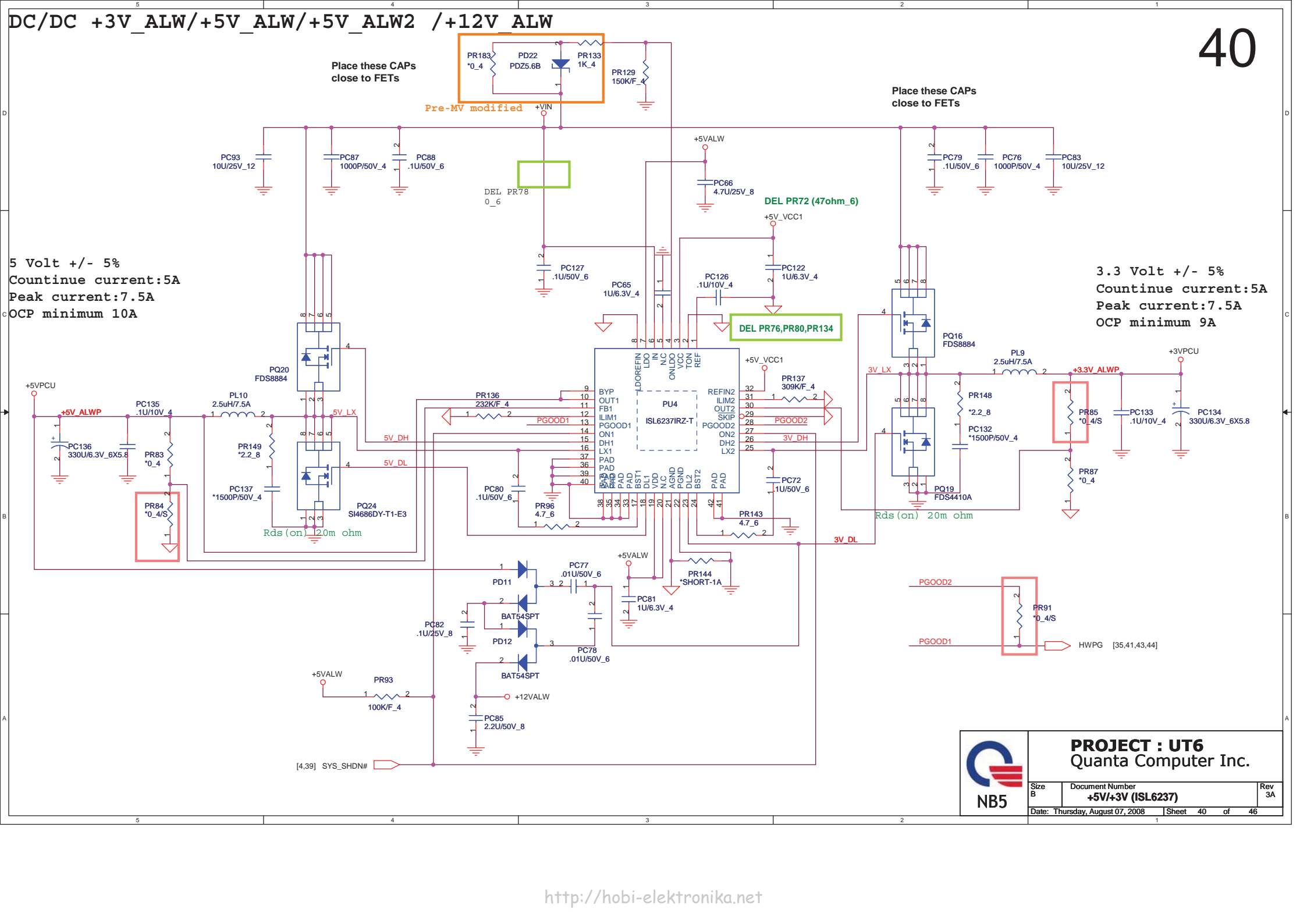
Place these CAPs close to FETs

Pre-MV modified +VIN

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OCP minimum 10A

3.3 Volt +/- 5%
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Peak current:7.5A
OCP minimum 9A



40

DC/DC +3V_ALW/+5V_ALW/+5V_ALW2 /+12V_ALW

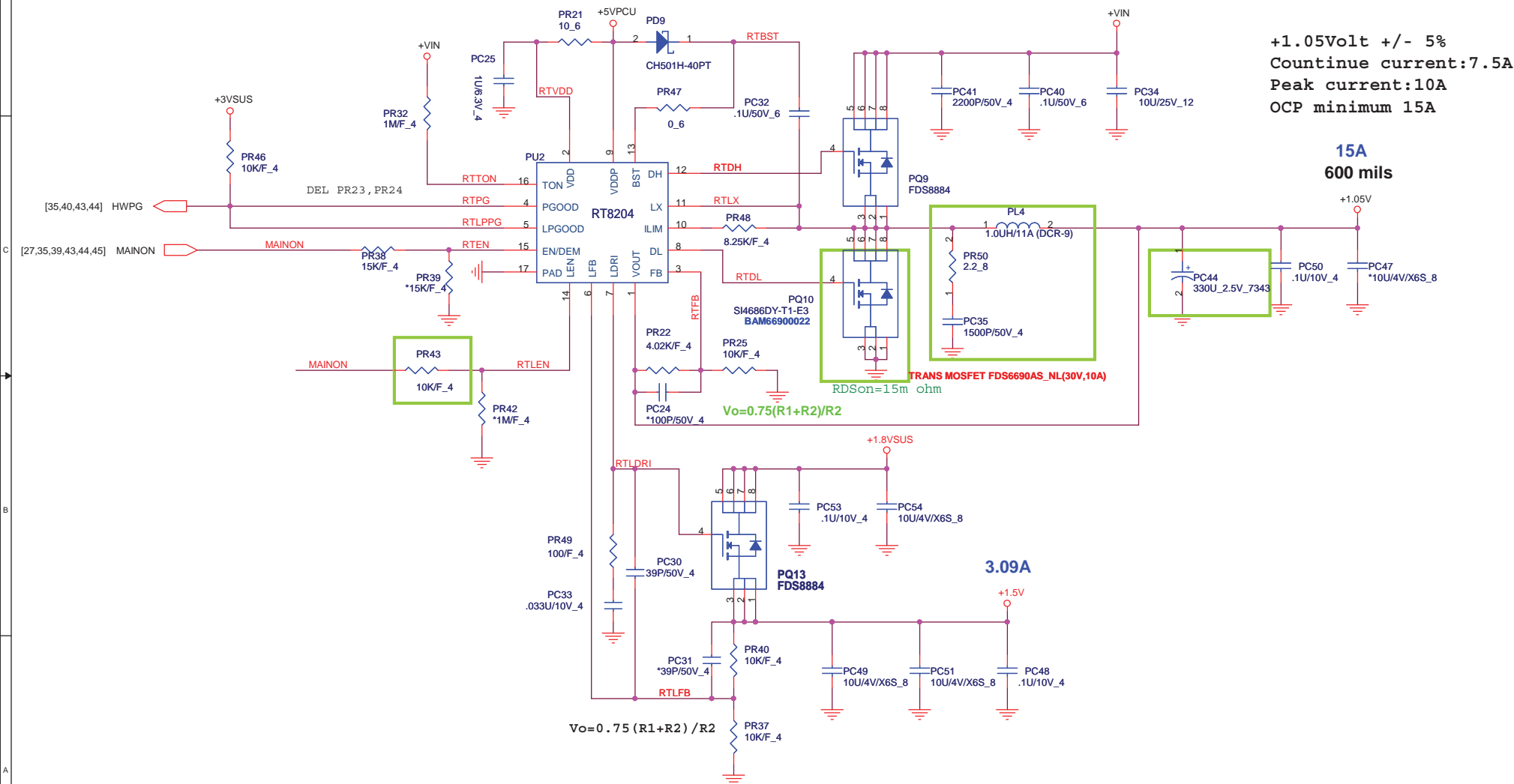
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Place these CAPs close to FETs

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Countinue current:5A
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OCP minimum 10A

3.3 Volt +/- 5%
Countinue current:5A
Peak current:7.5A
OCP minimum 9A



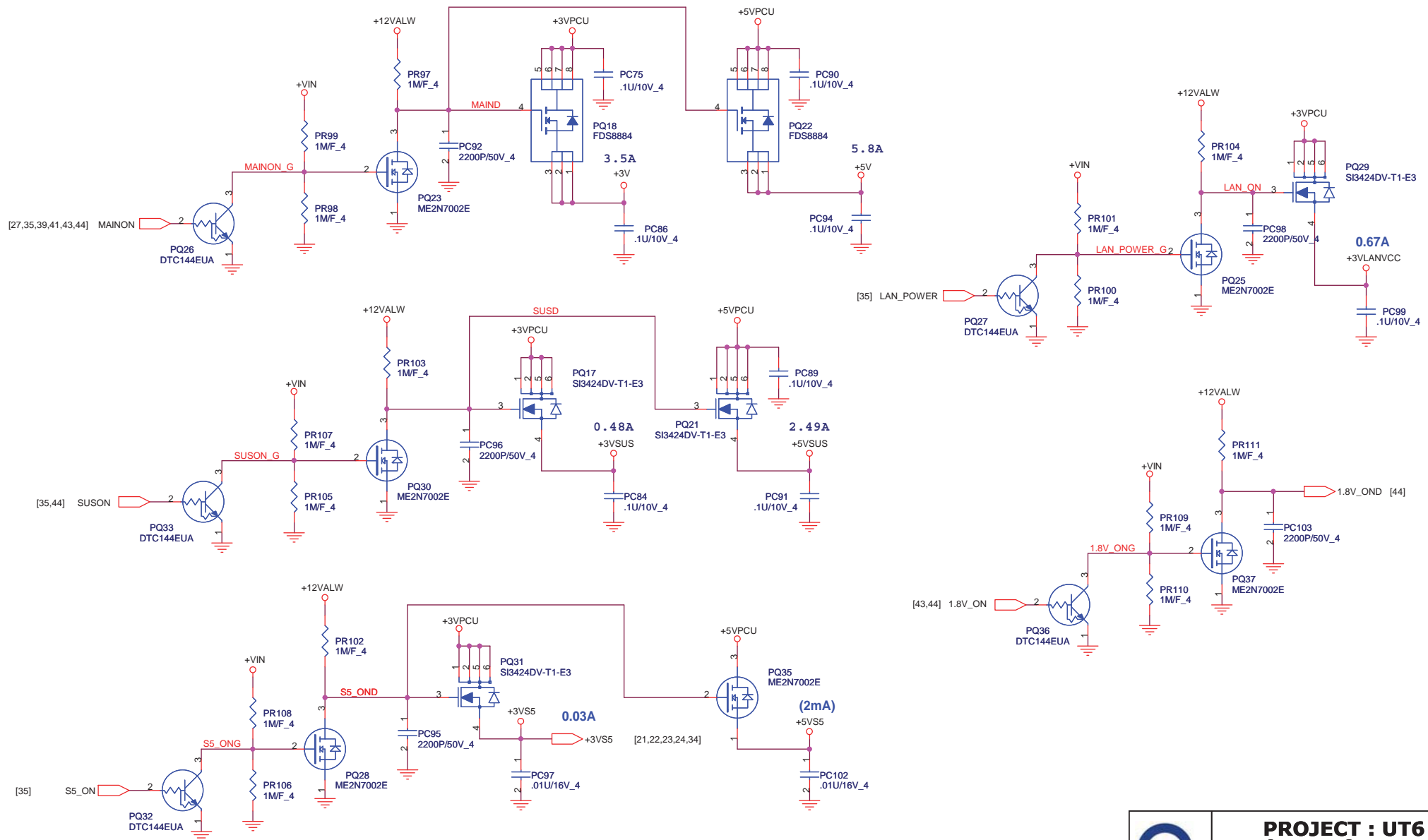
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Quanta Computer Inc.

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B	+1.05V/+1.5V (RT8204)	3A
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	Voltage level	AC MODE				DC MODE			
		S0	S3	S4	S5	S0	S3	S4	S5
+3VPCU	3.3V +/- 5%	V	V	V	V	V	V	V	V
+5VPCU	5V +/- 5%	V	V	V	V	V	V	V	V
+3VRTC	3.3V +/- 5%	V	V	V	V	V	V	V	V
+3VS5	3.3V +/- 5%	V	V	V	V	V	V		
+5VS5	5V +/- 5%	V	V	V	V	V	V		
+3VSUS	3.3V +/- 5%	V	V			V	V		
+5VSUS	5V +/- 5%	V	V			V	V		
+1.8VSUS	1.8V +/- 5%	V	V			V	V		
+0.9VSMVTT	0.9V +/- 5%	V	V			V	V		
+1.5V	1.5V +/- 5%	V				V			
+1.05V	1.05V +/- 5%	V				V			
+VCORE	0.9~1.15V	V				V			
+VGA_CORE	0.9~1.2V	V				V			
+VGA1.1V	1.1V +/- 5%	V				V			
+1.8V	1.8V +/- 5%	V				V			
+3VLAVCC	3.3V +/- 5%	V				V			



NB5

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Size
CustomDocument Number
VoltageRev
E3A

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